

Fig. 1

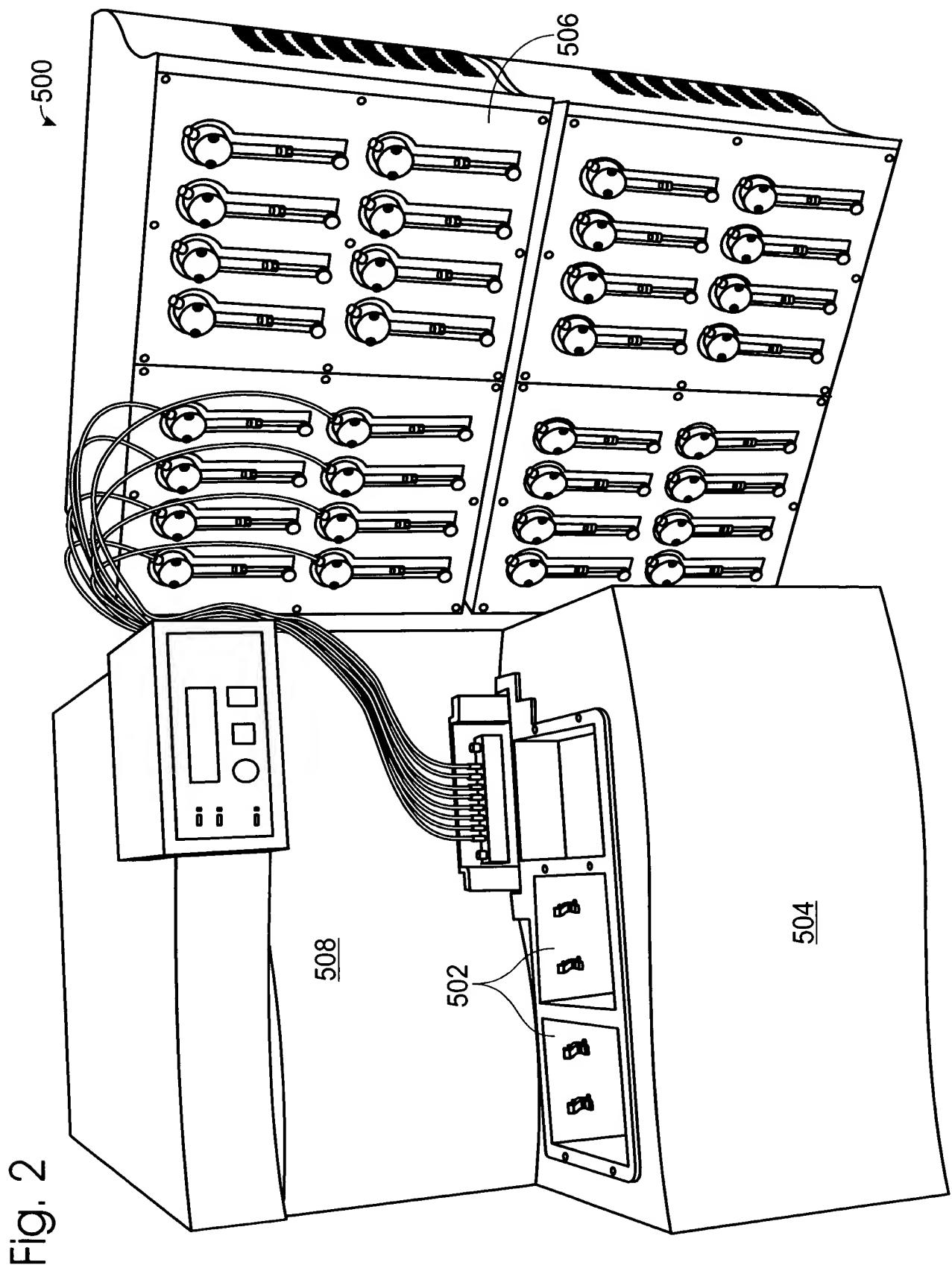


Fig. 3

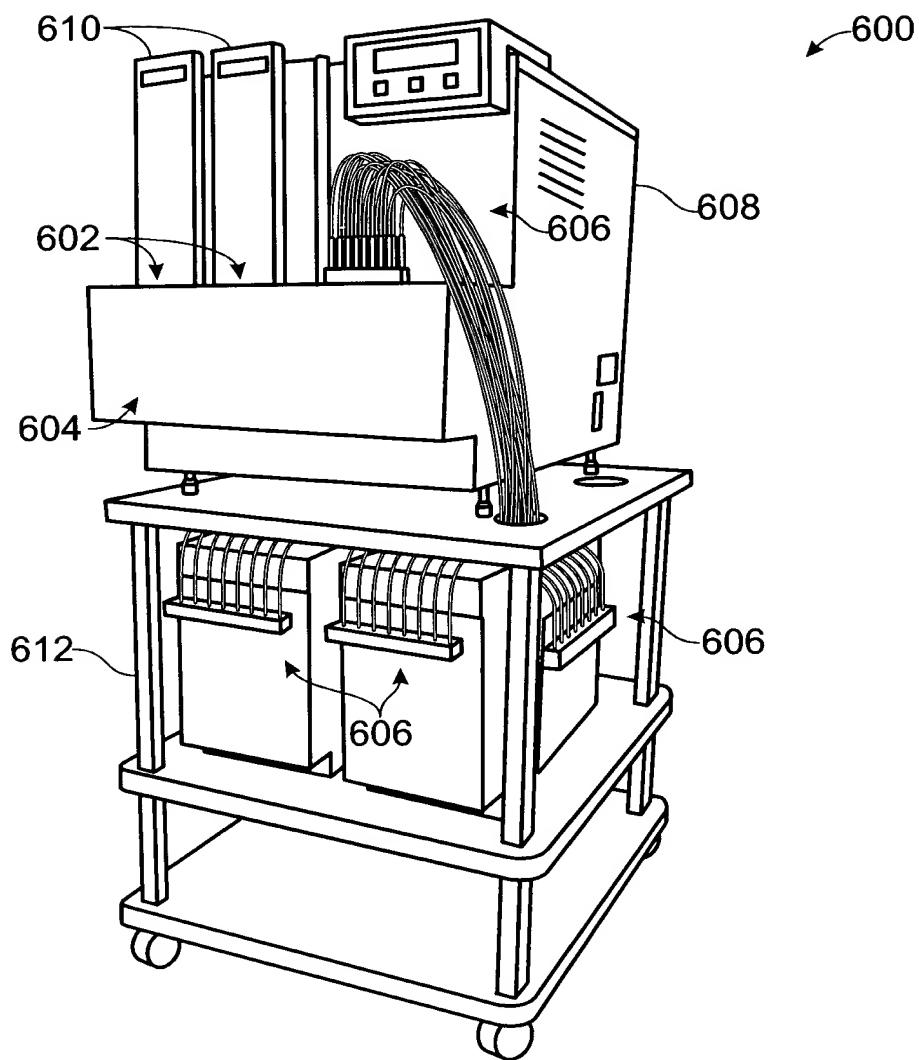
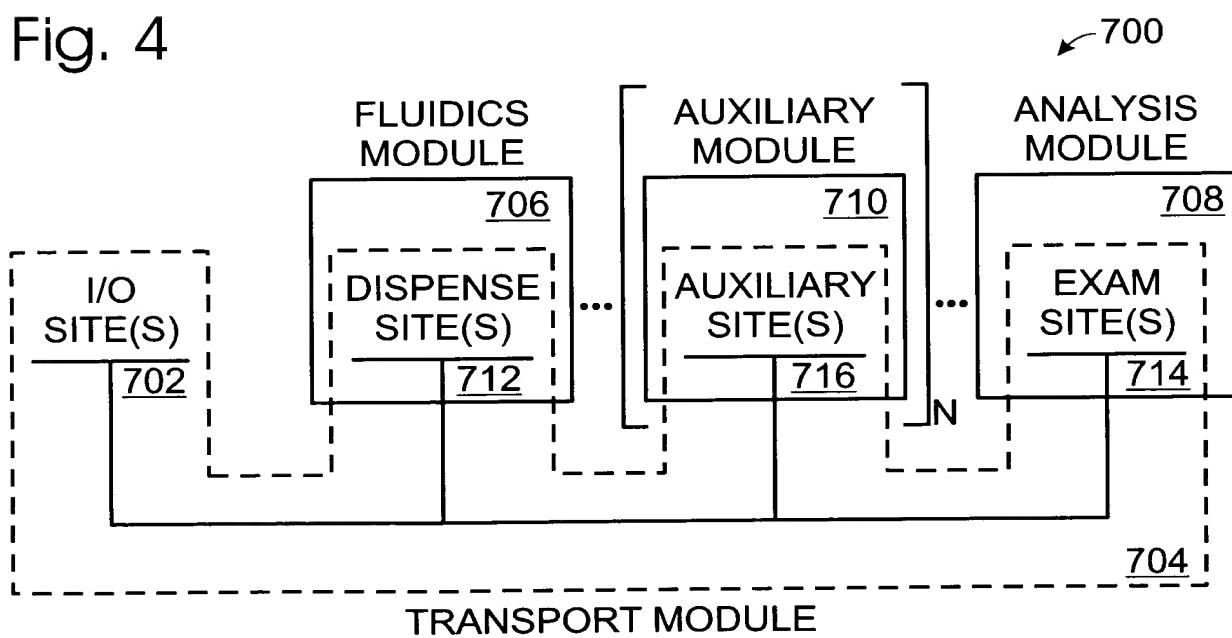
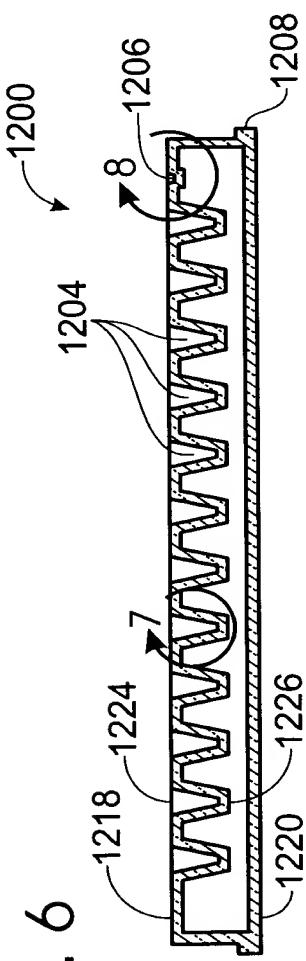
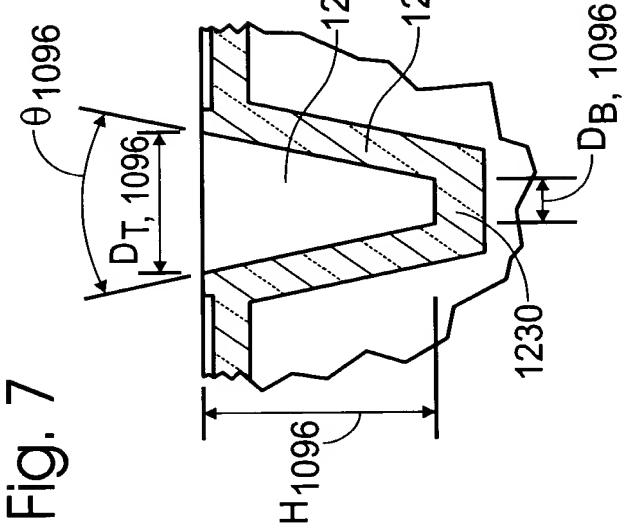
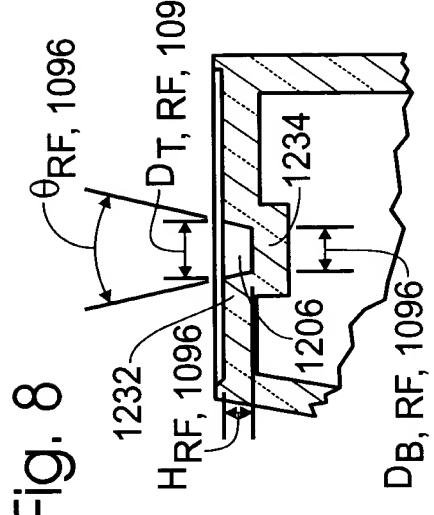
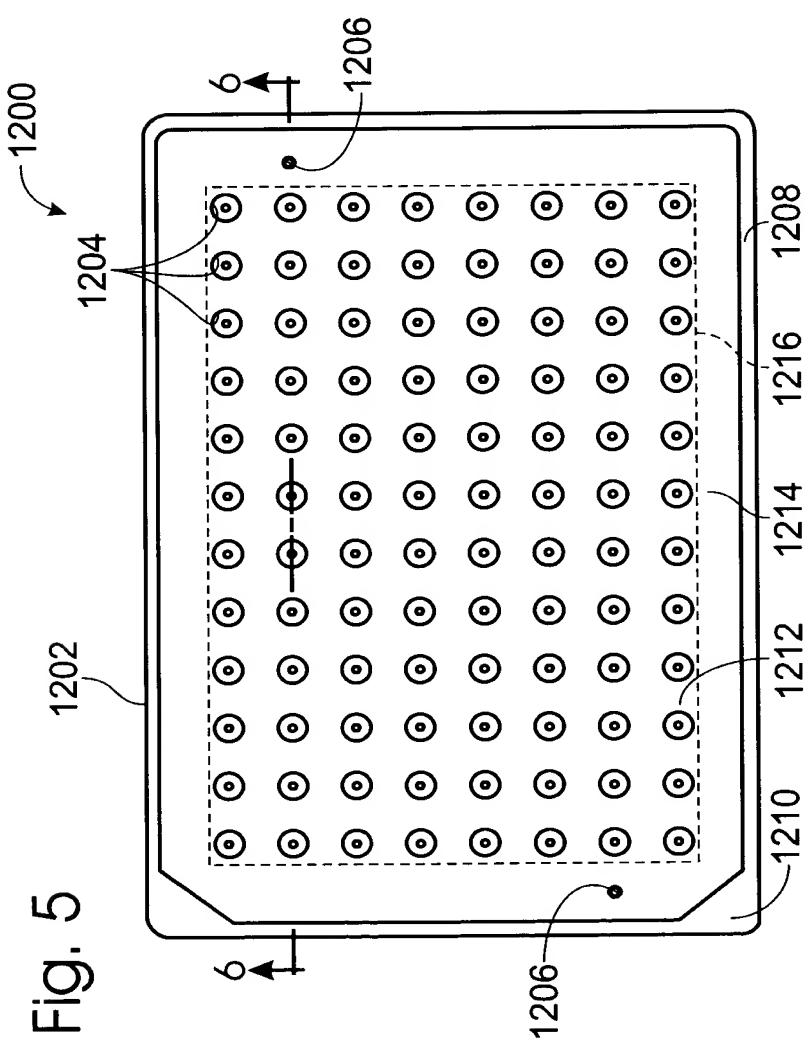
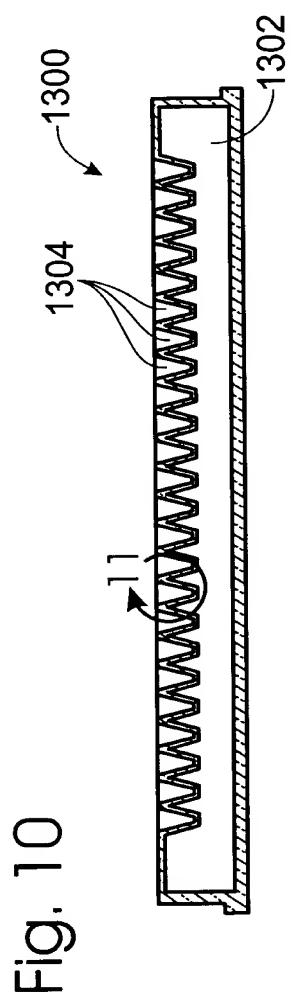
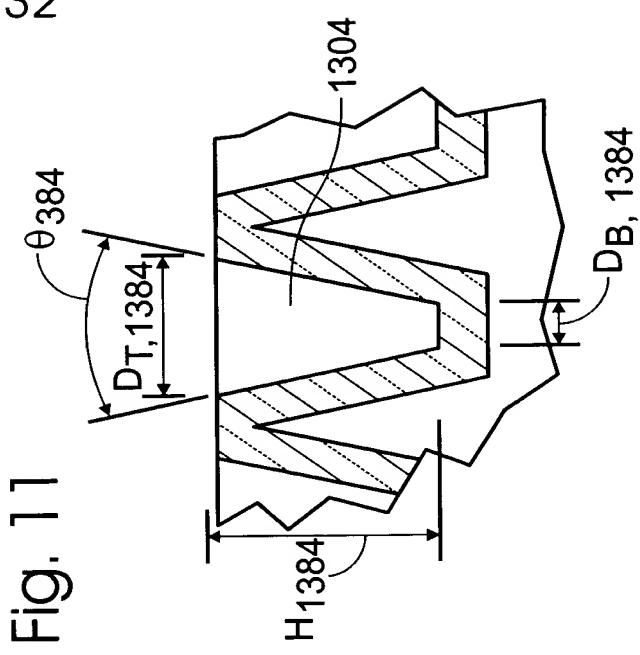
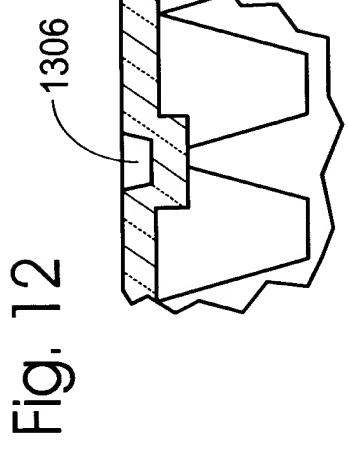
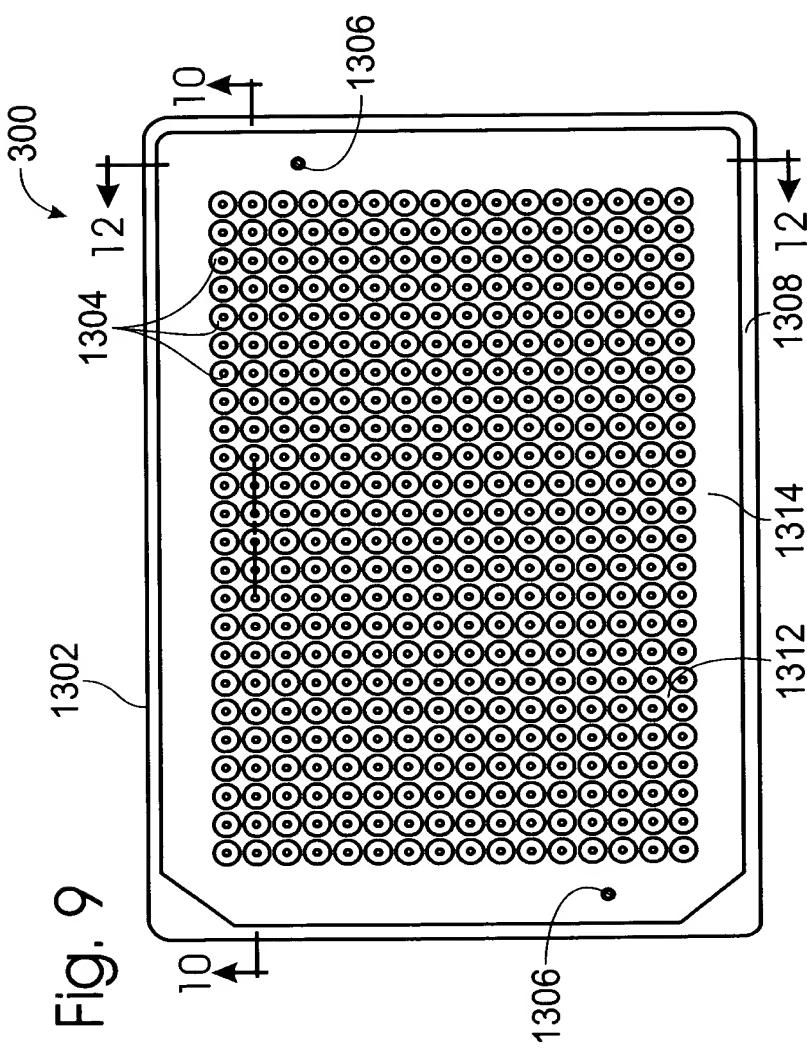
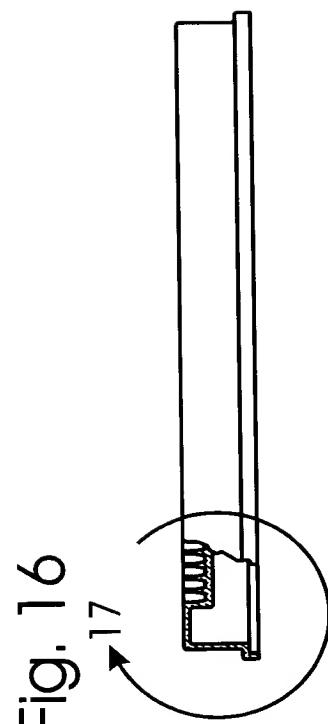
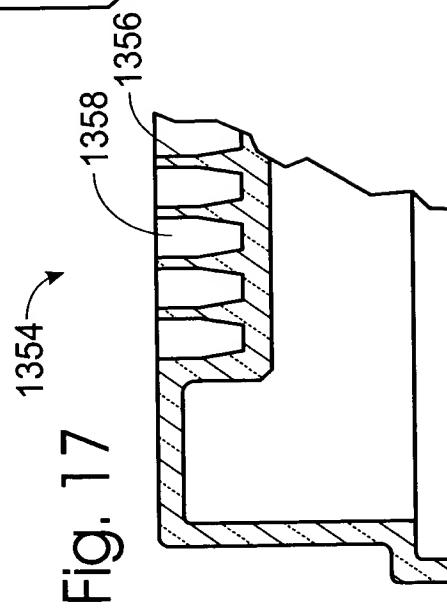
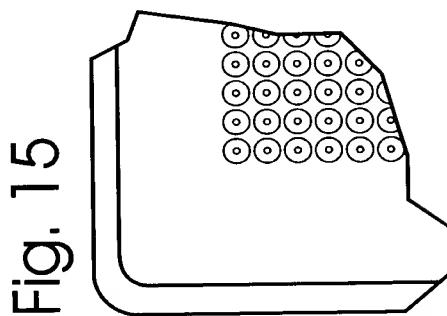
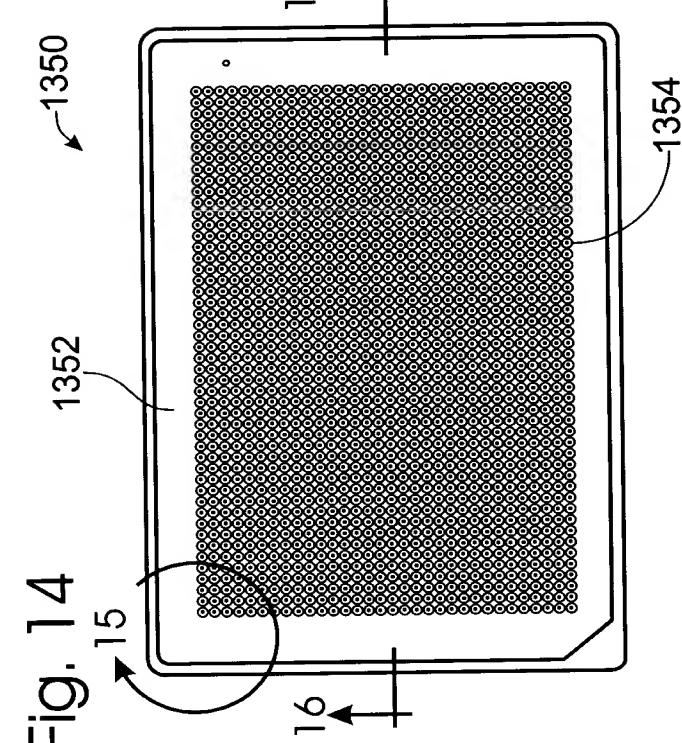
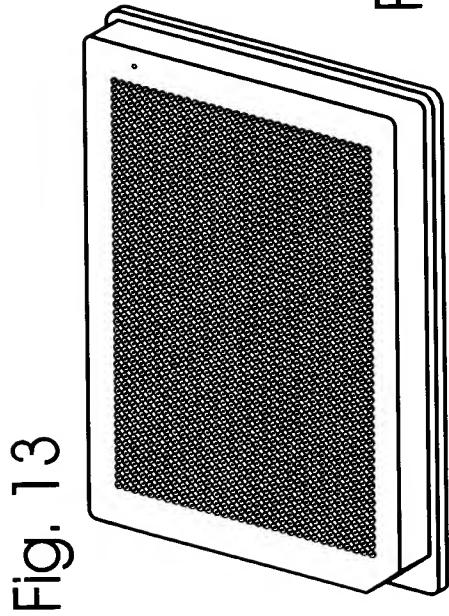


Fig. 4









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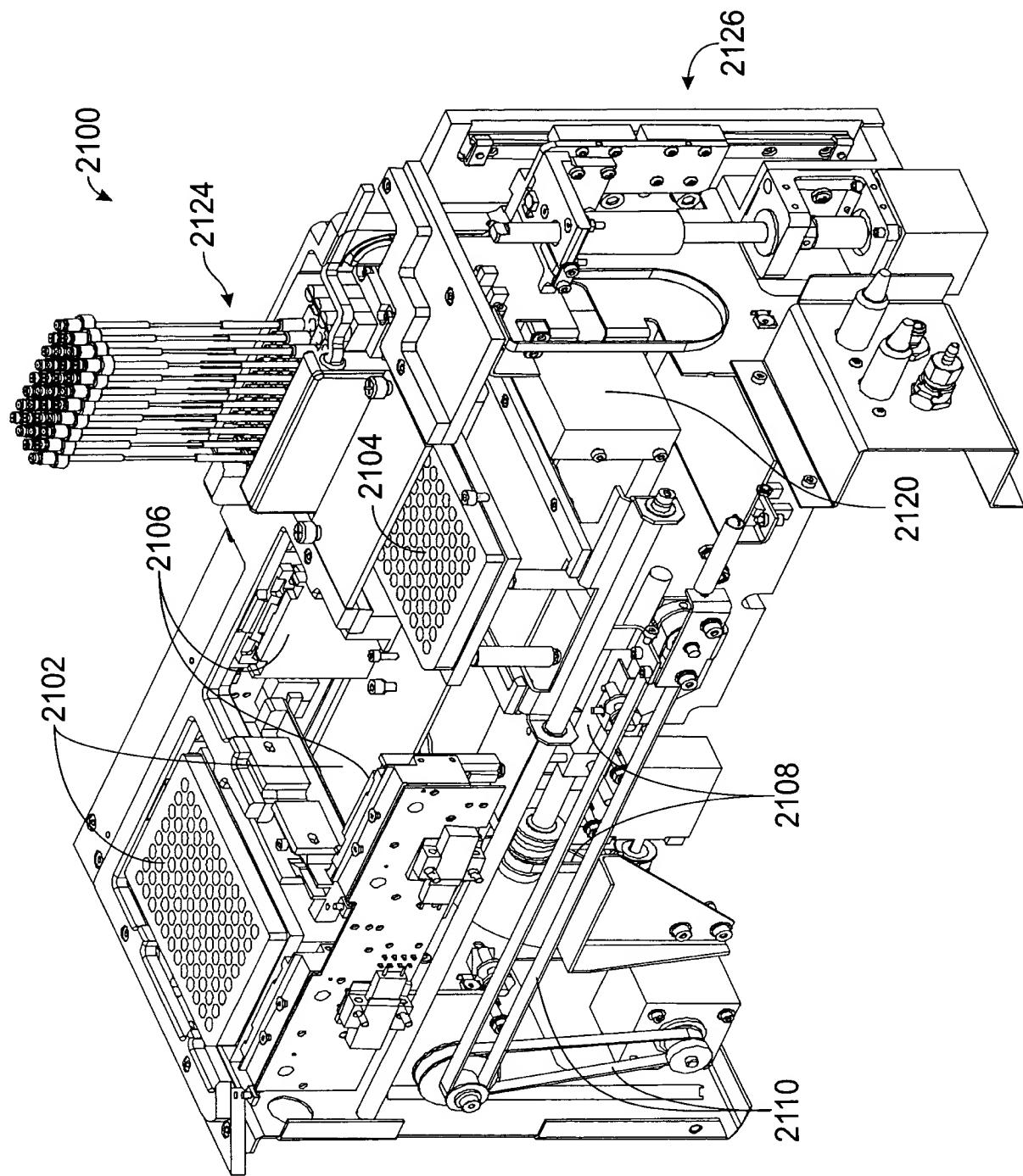


Fig. 18

Fig. 19

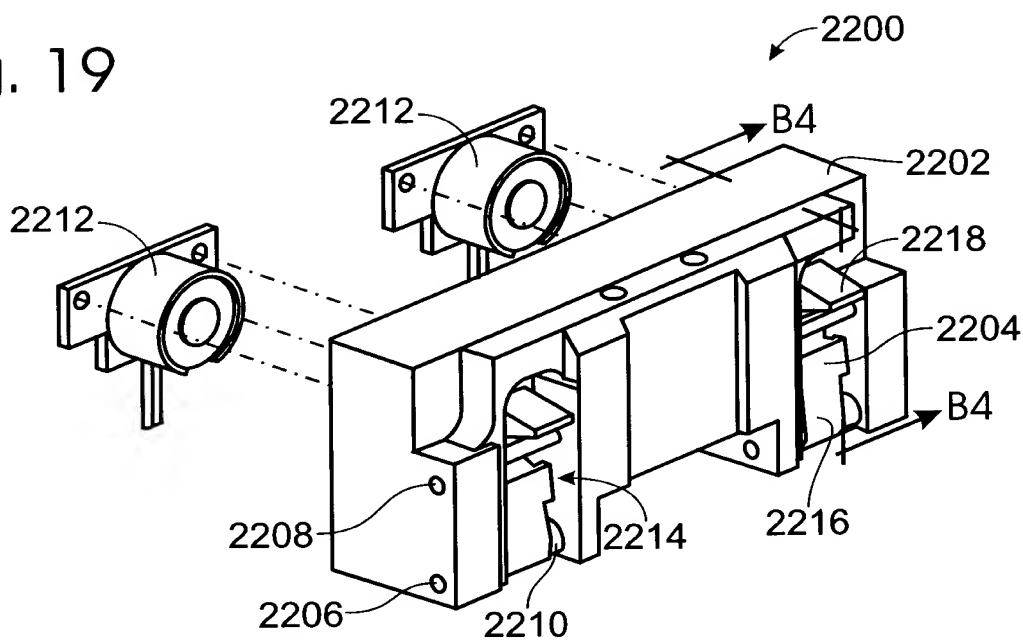


Fig. 20

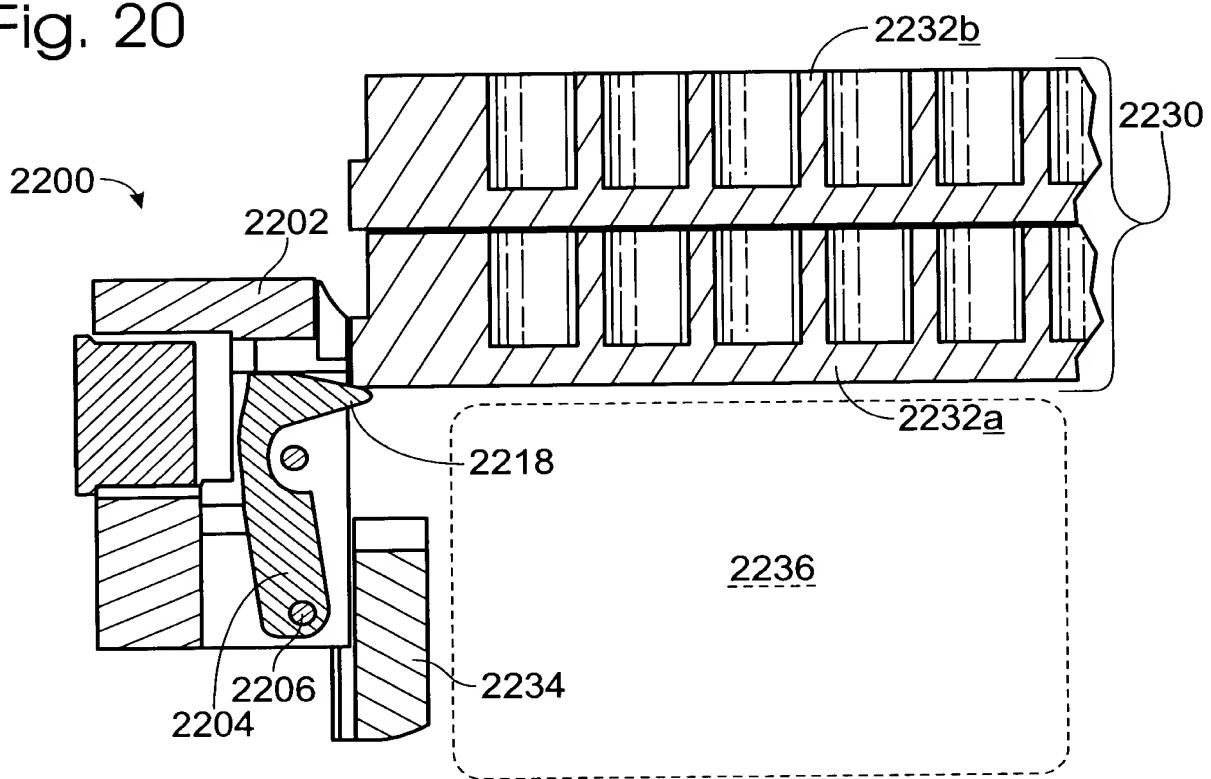


Fig. 21 (INPUT CYCLE)

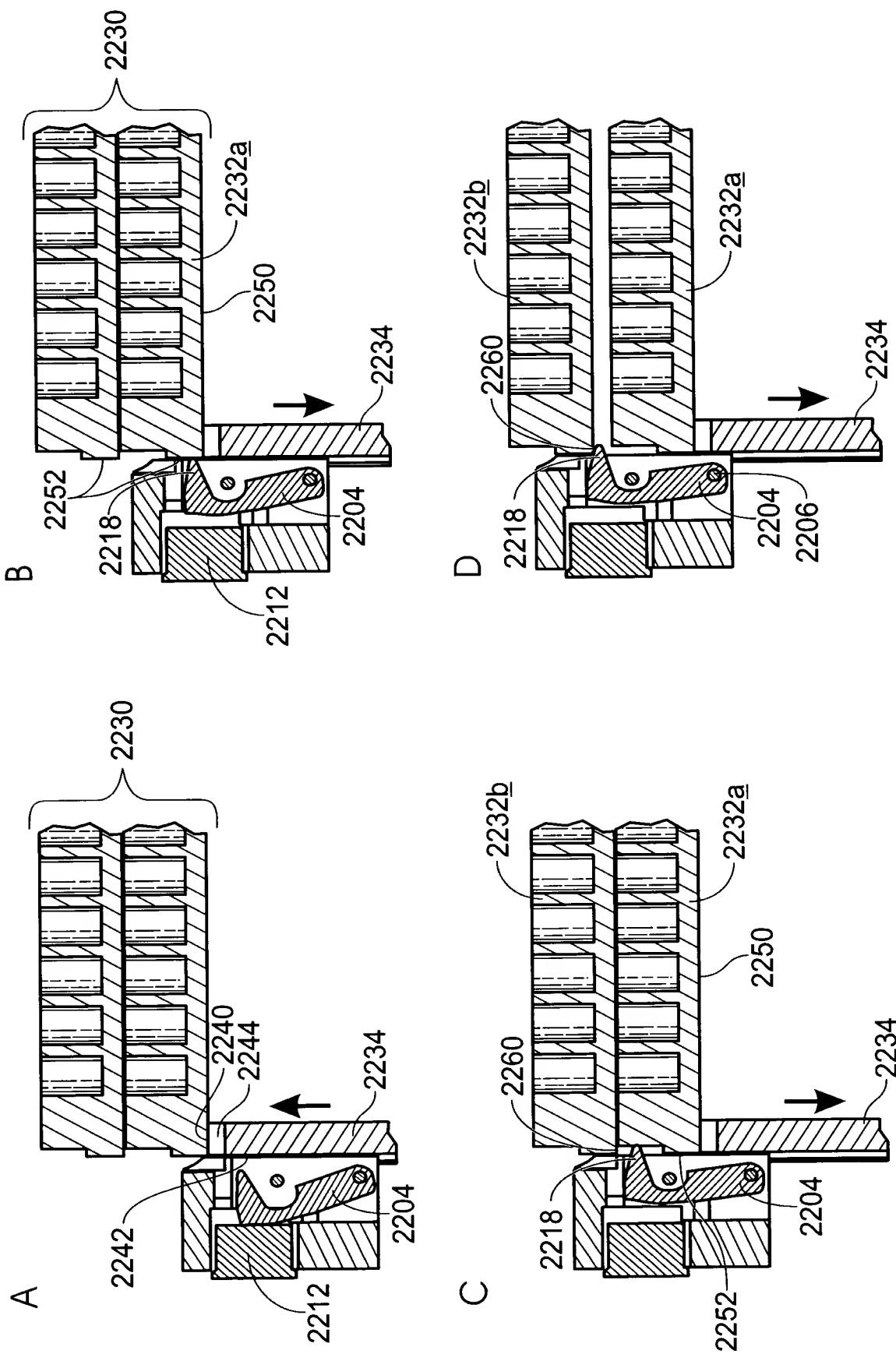


Fig. 22 (OUTPUT CYCLE)

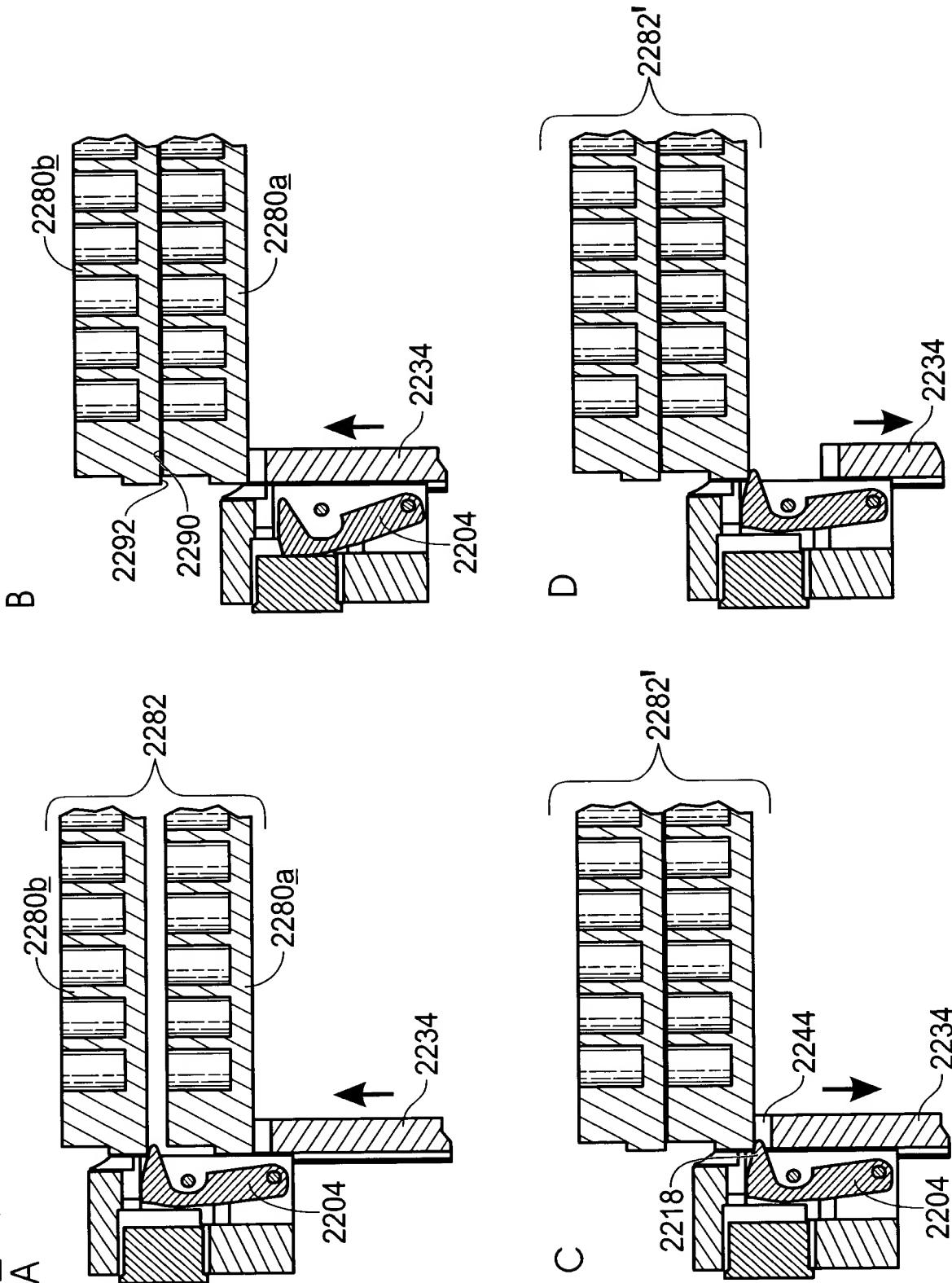


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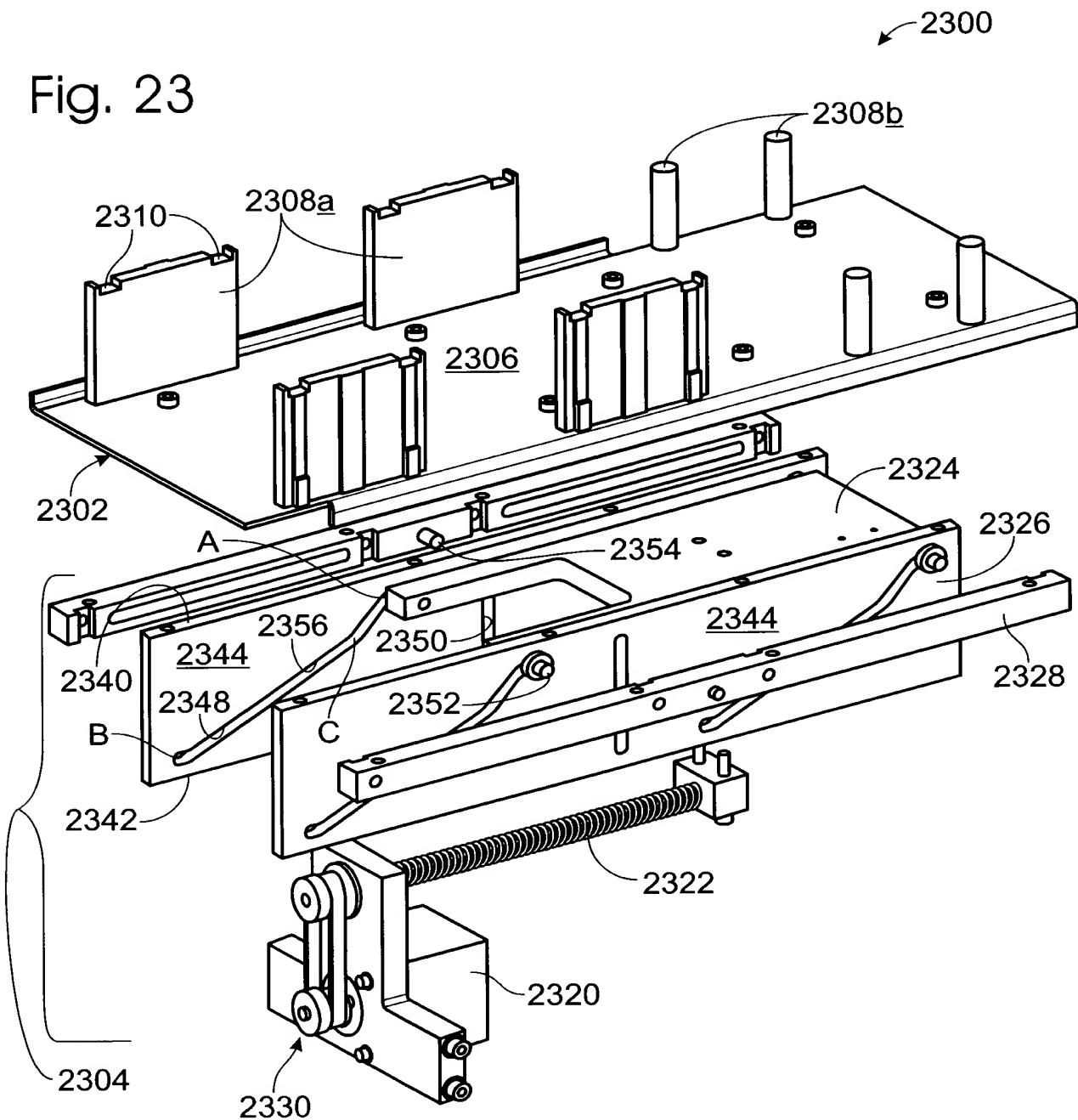


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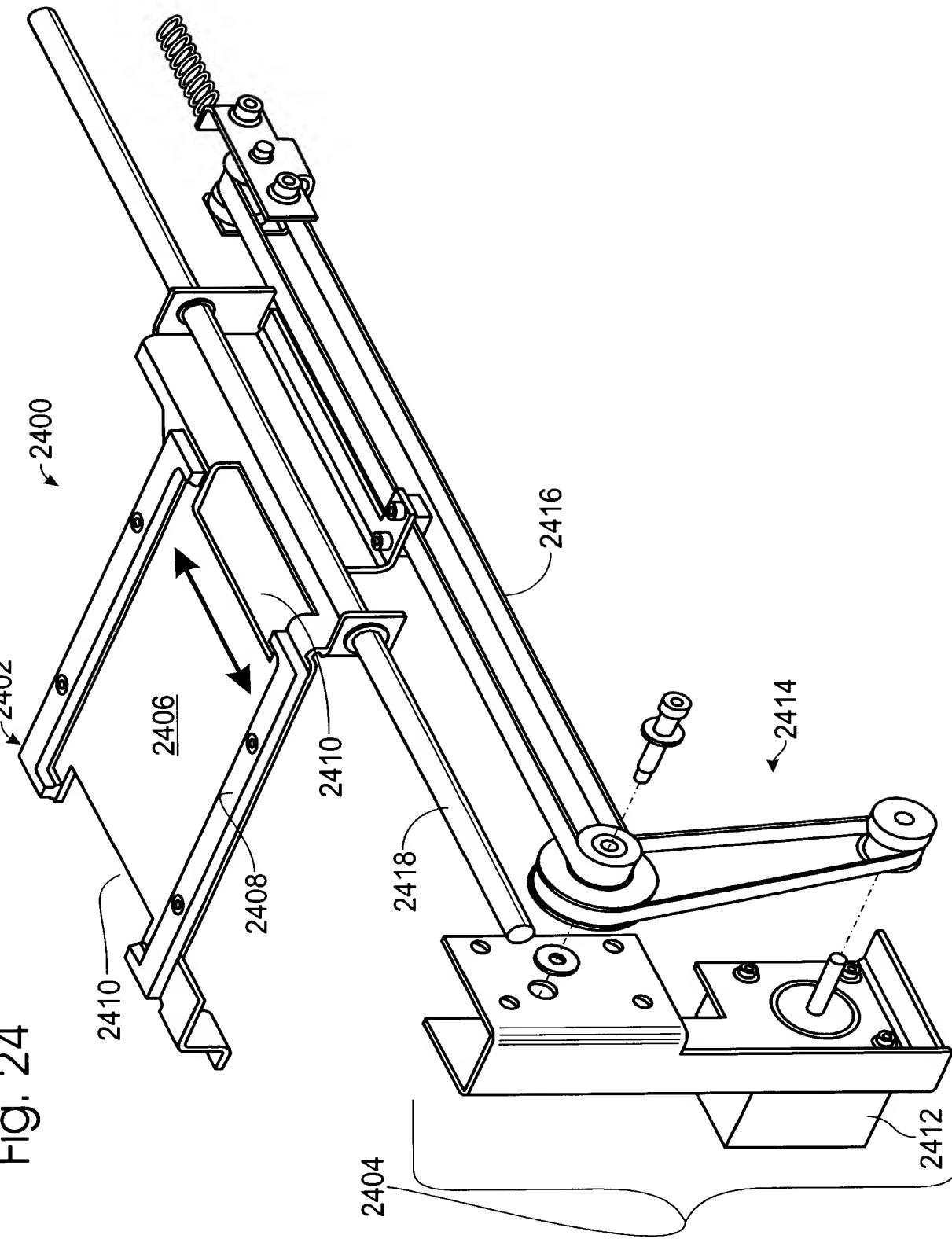


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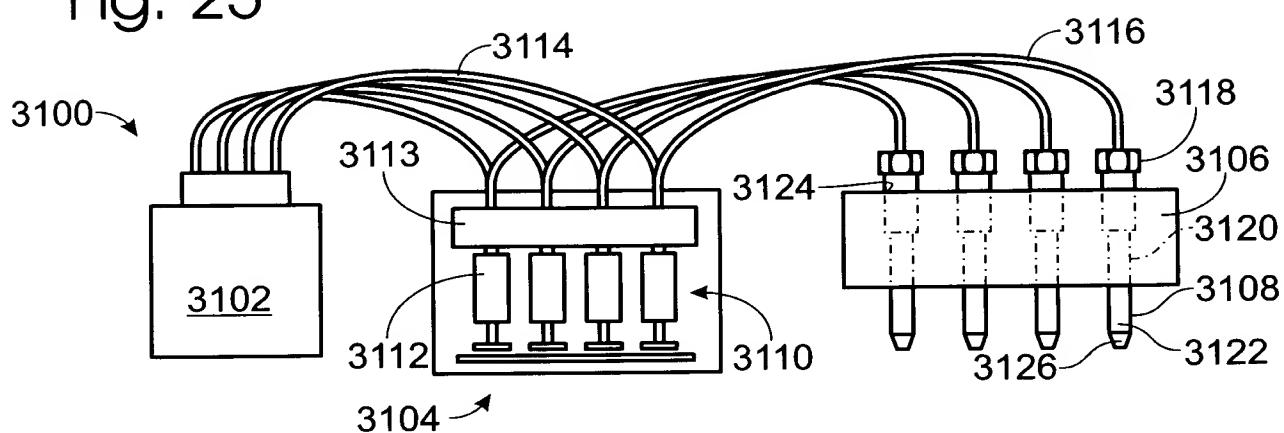


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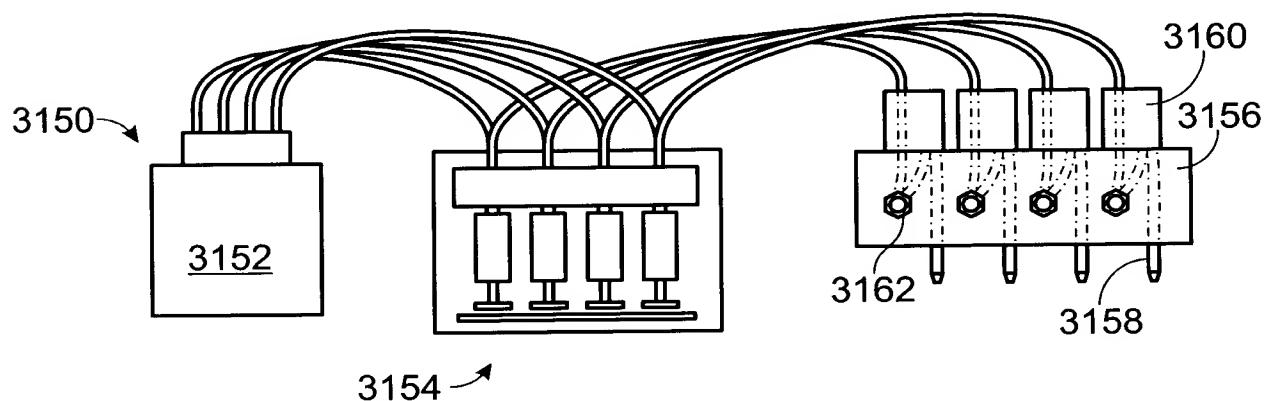


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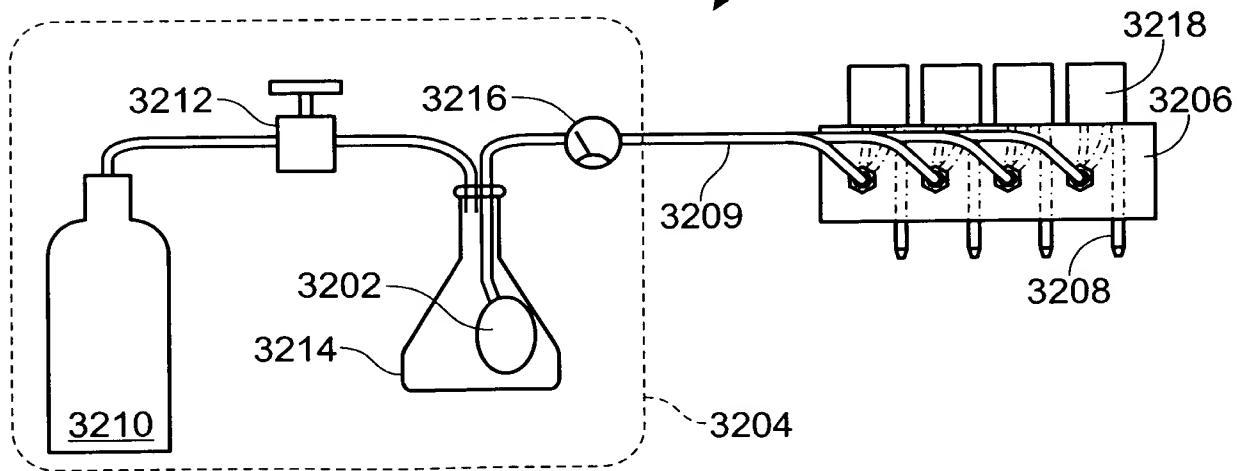


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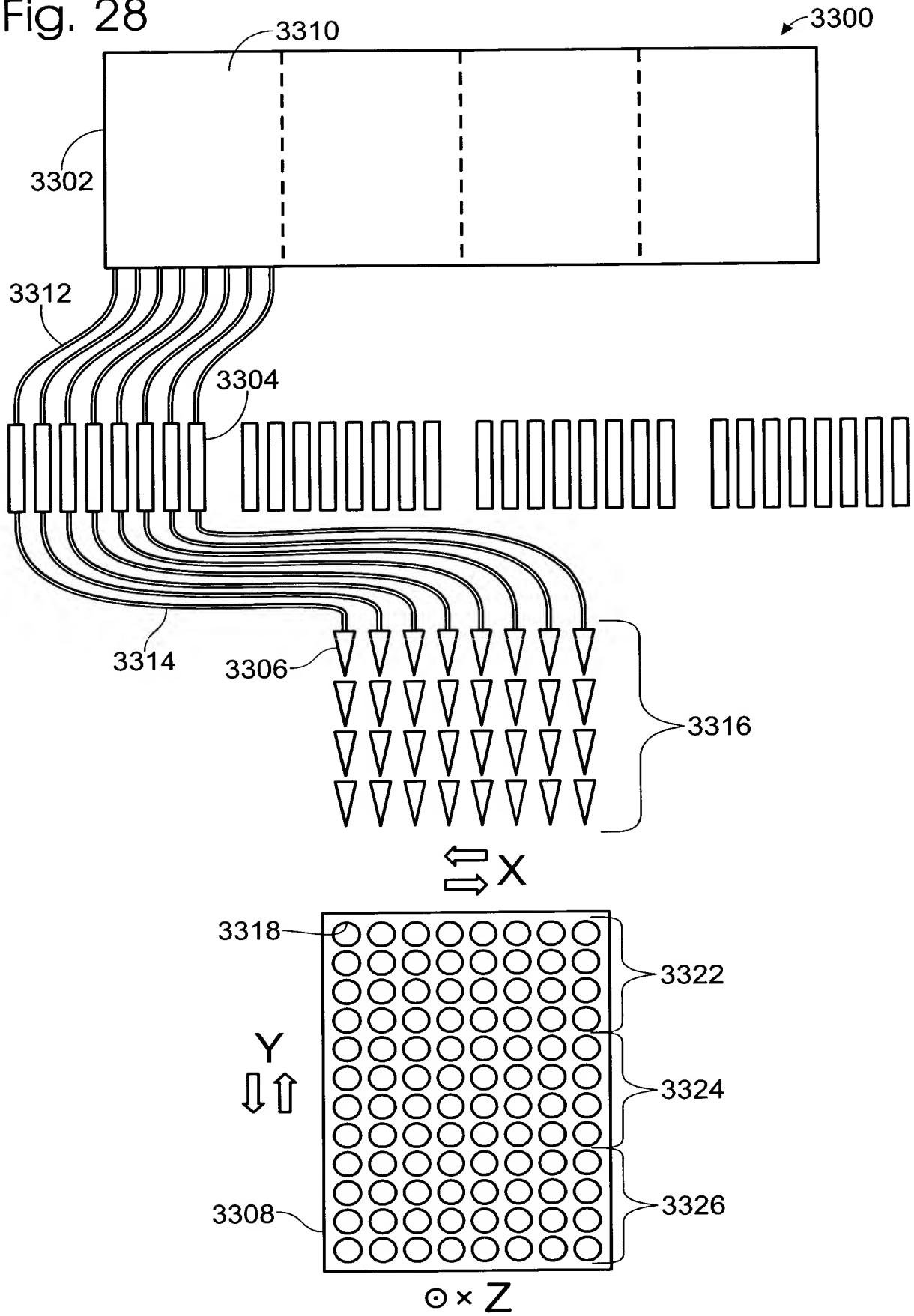


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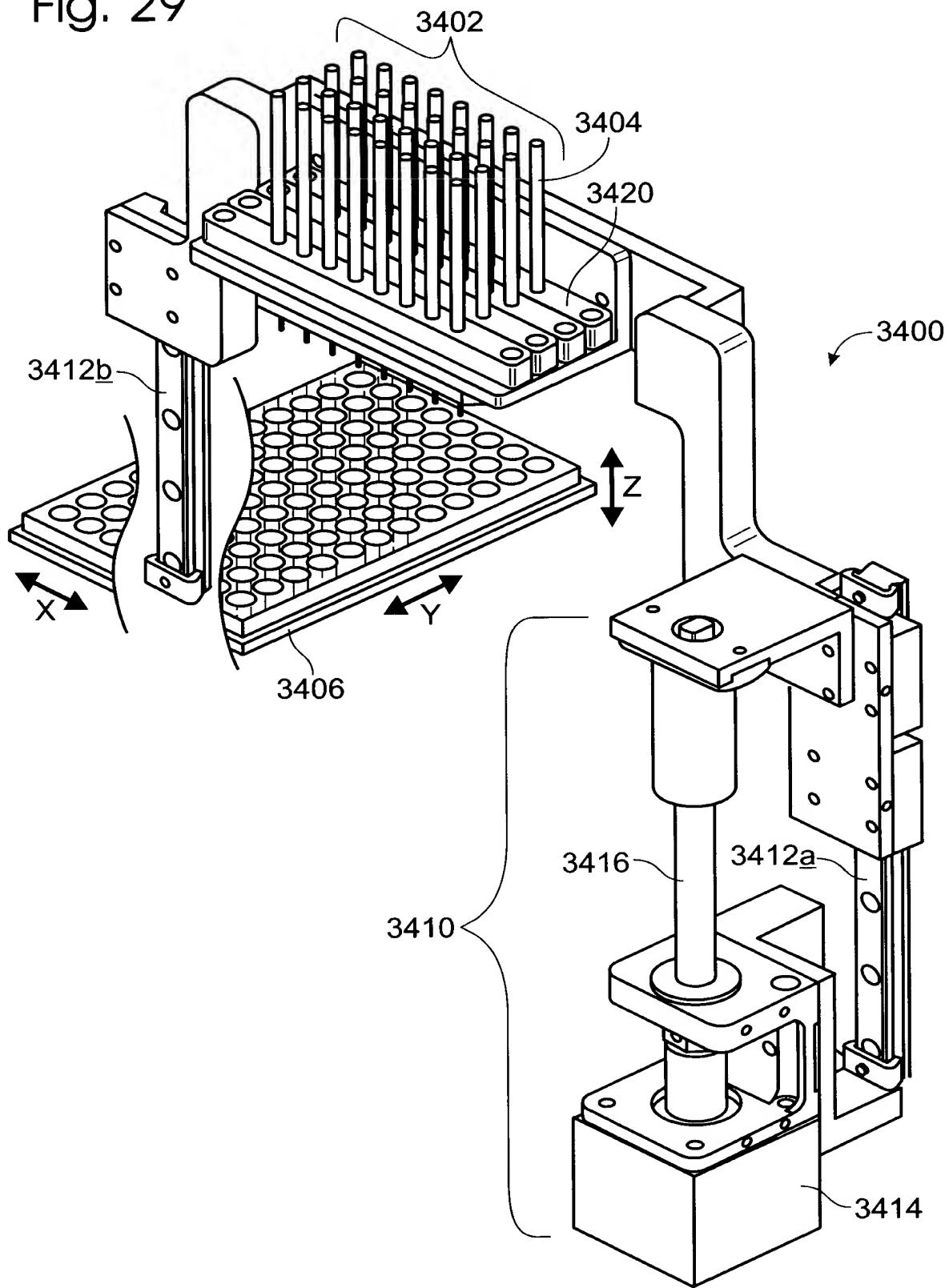


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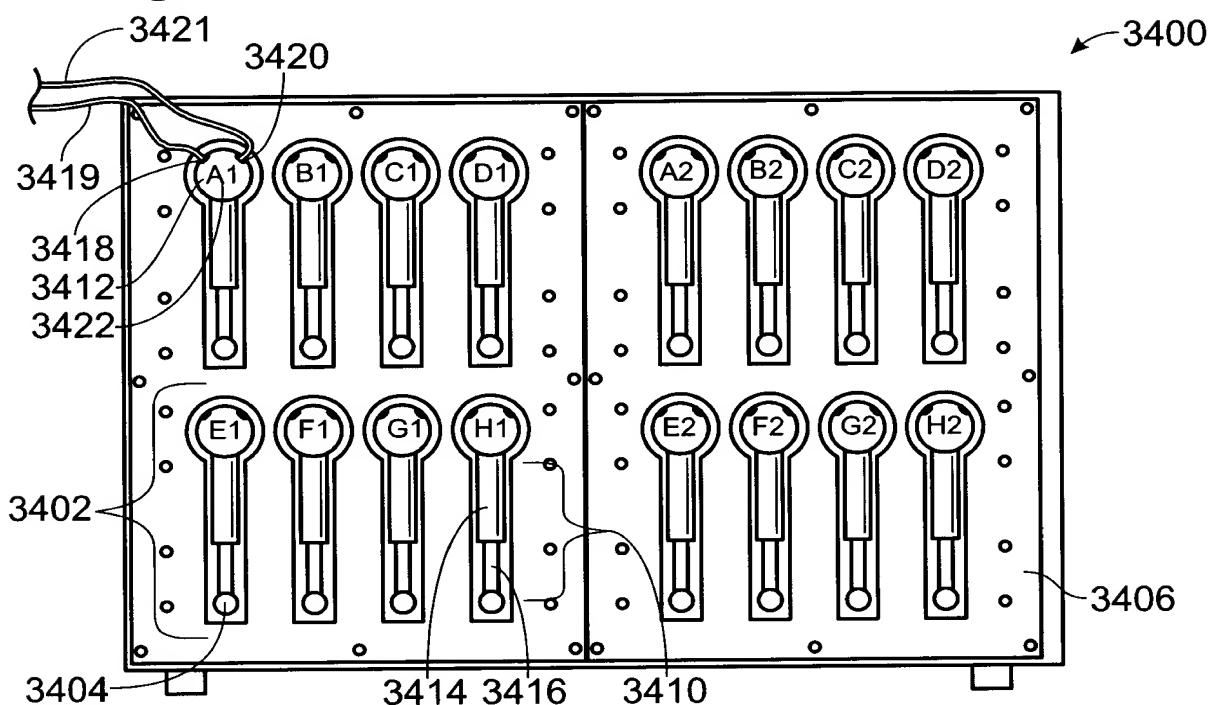


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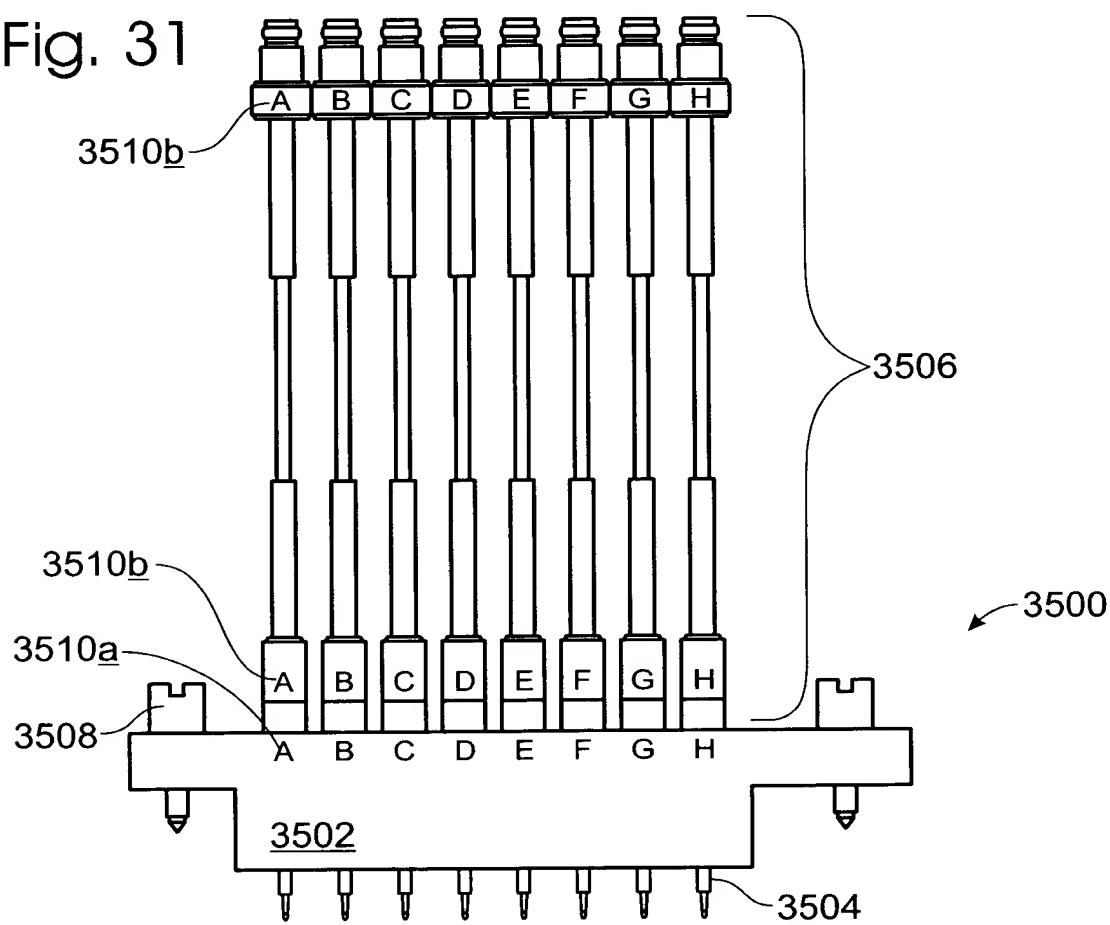


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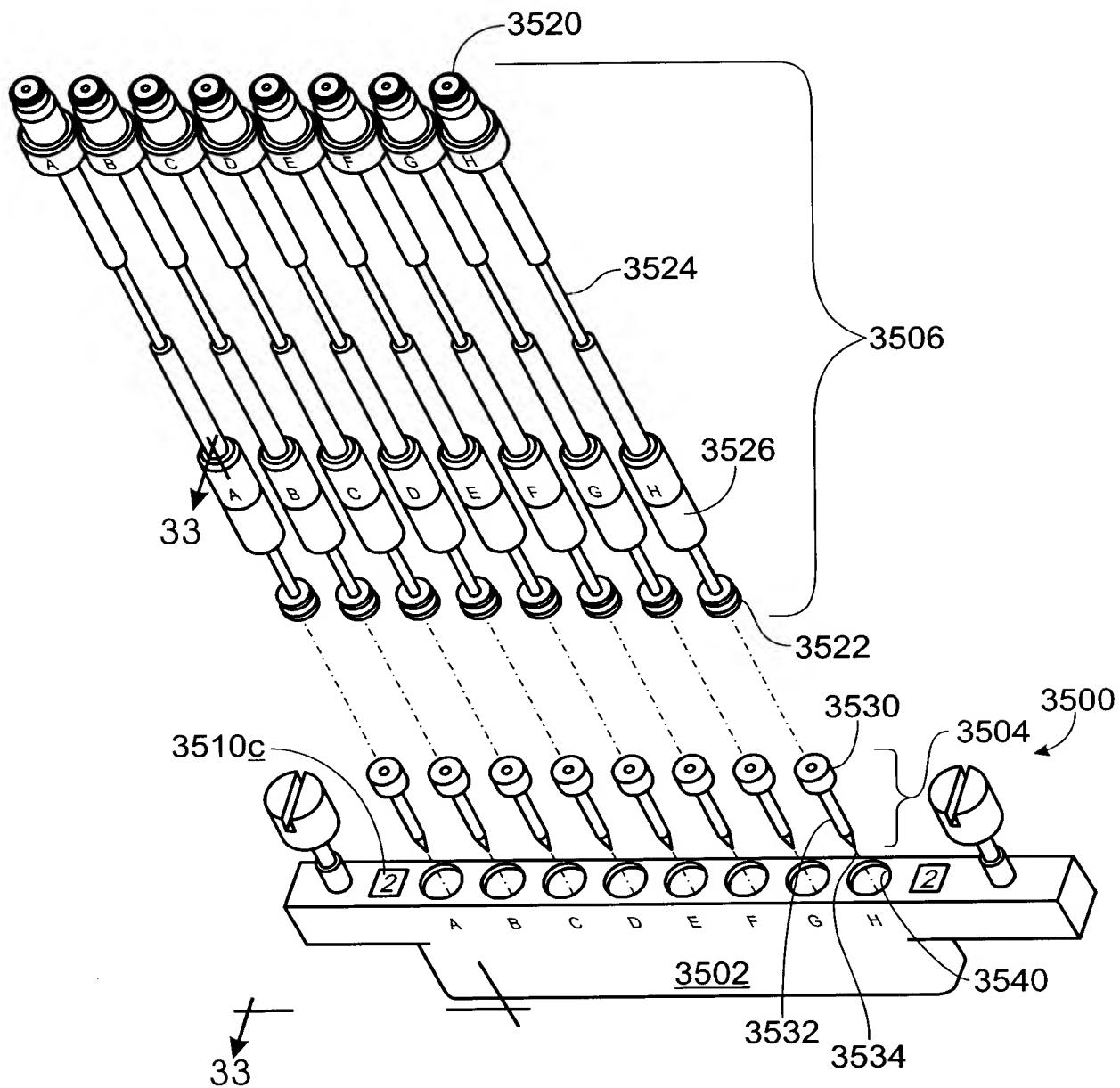


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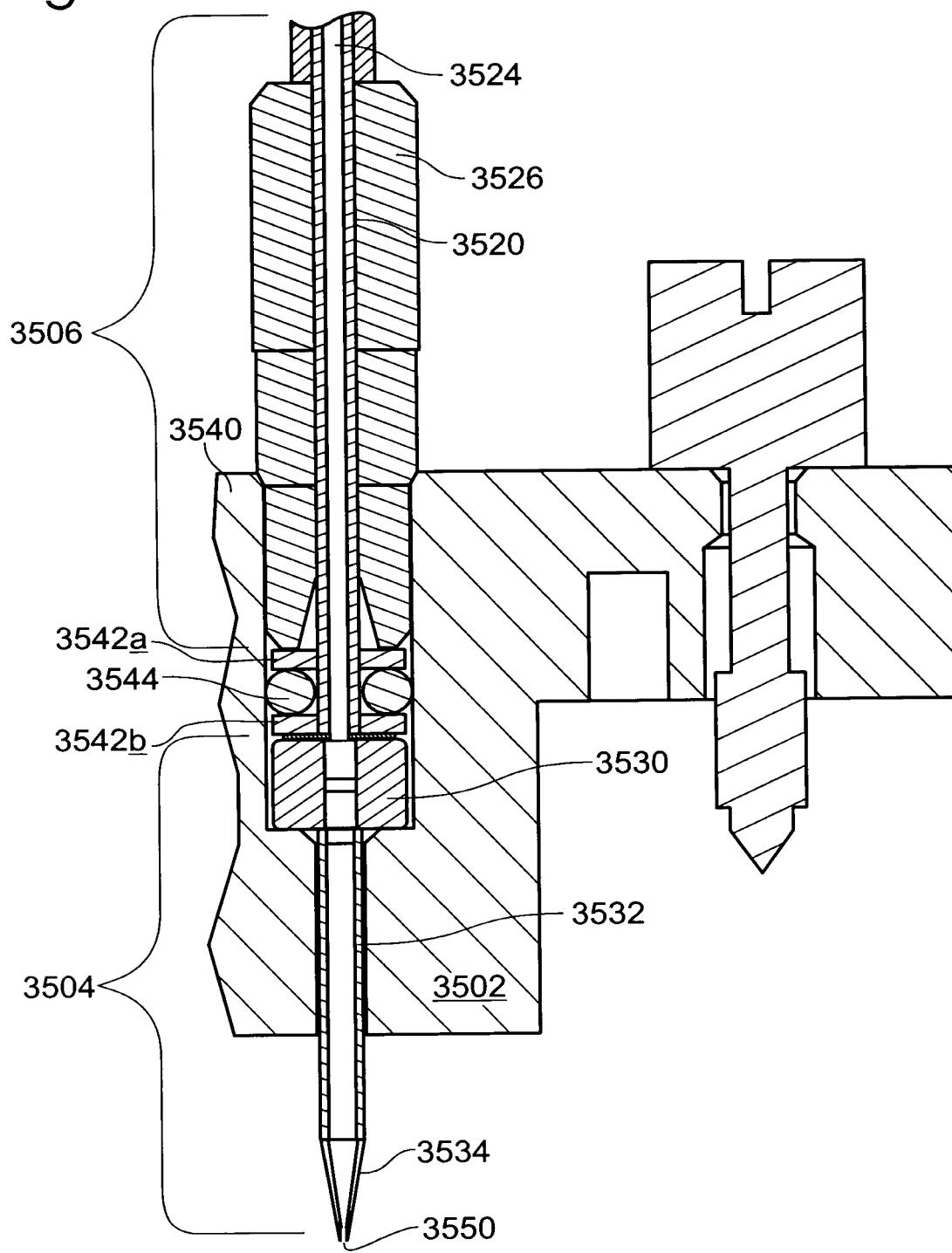


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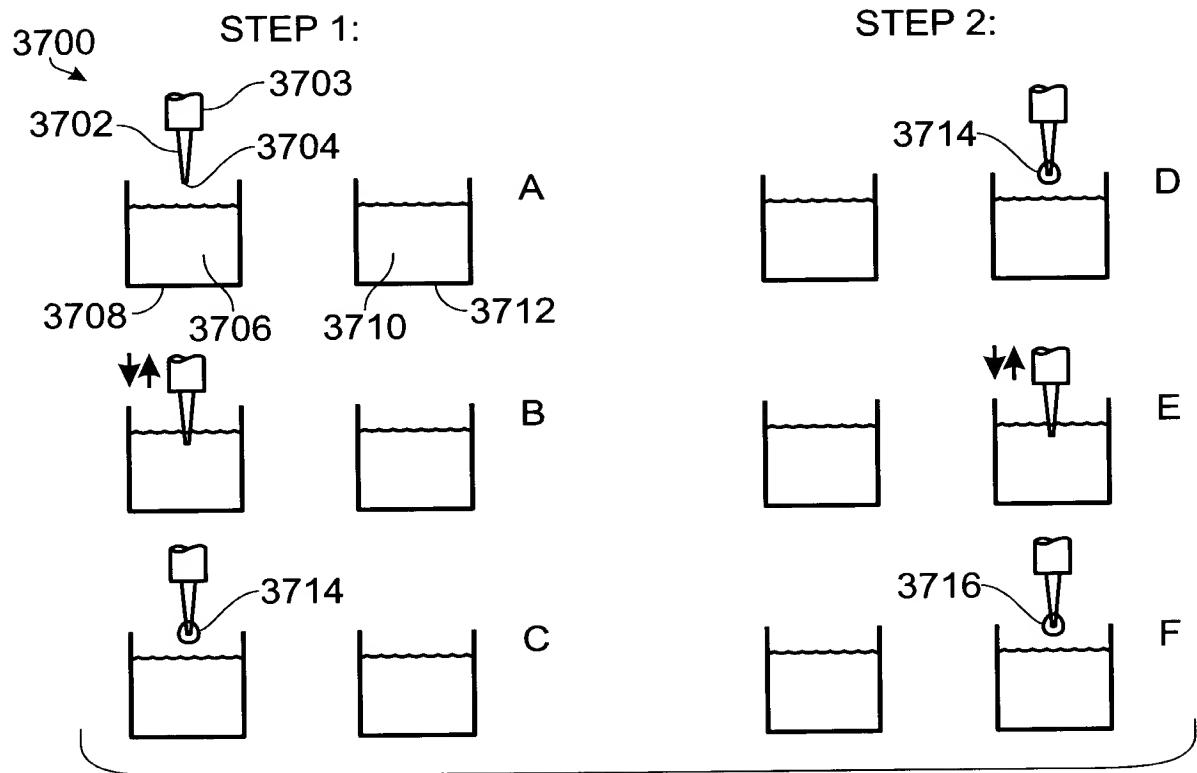


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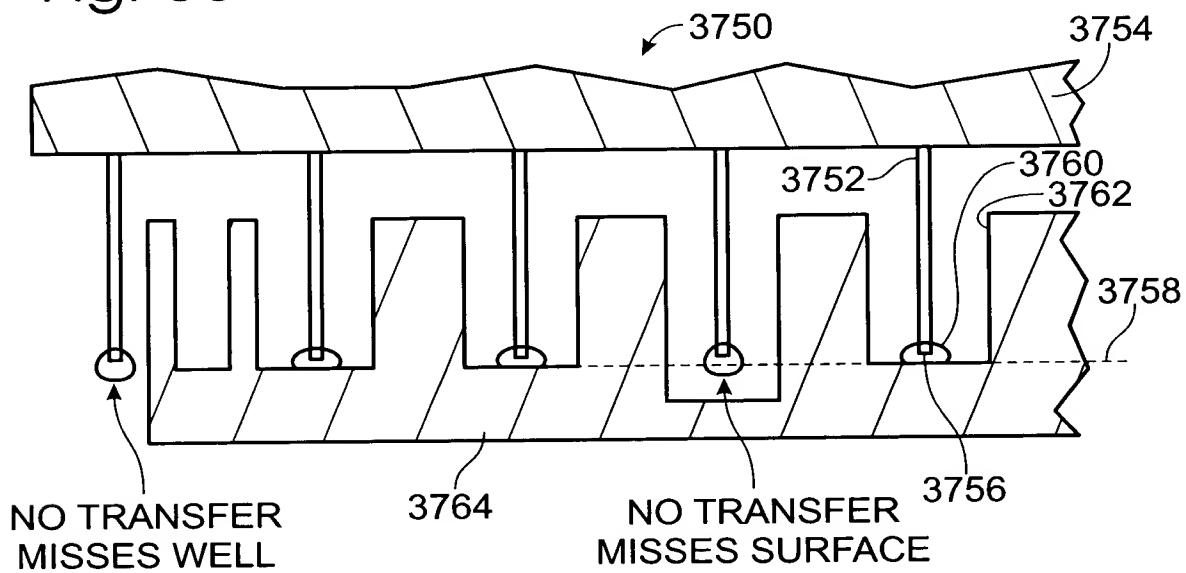


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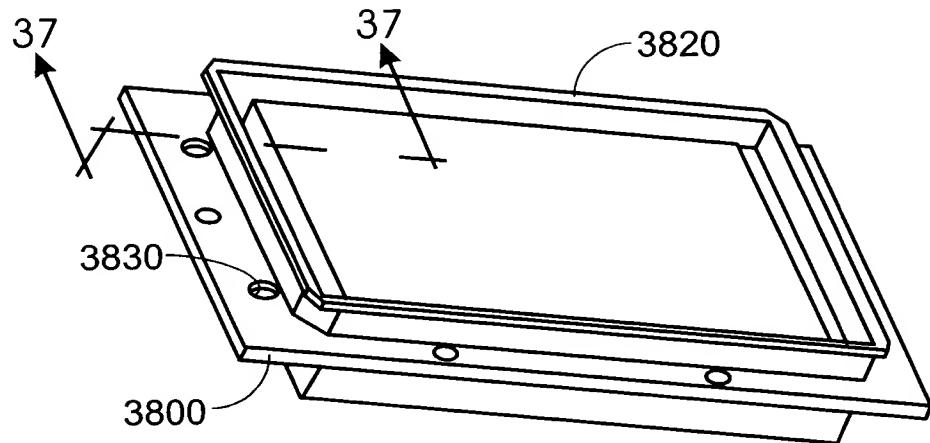


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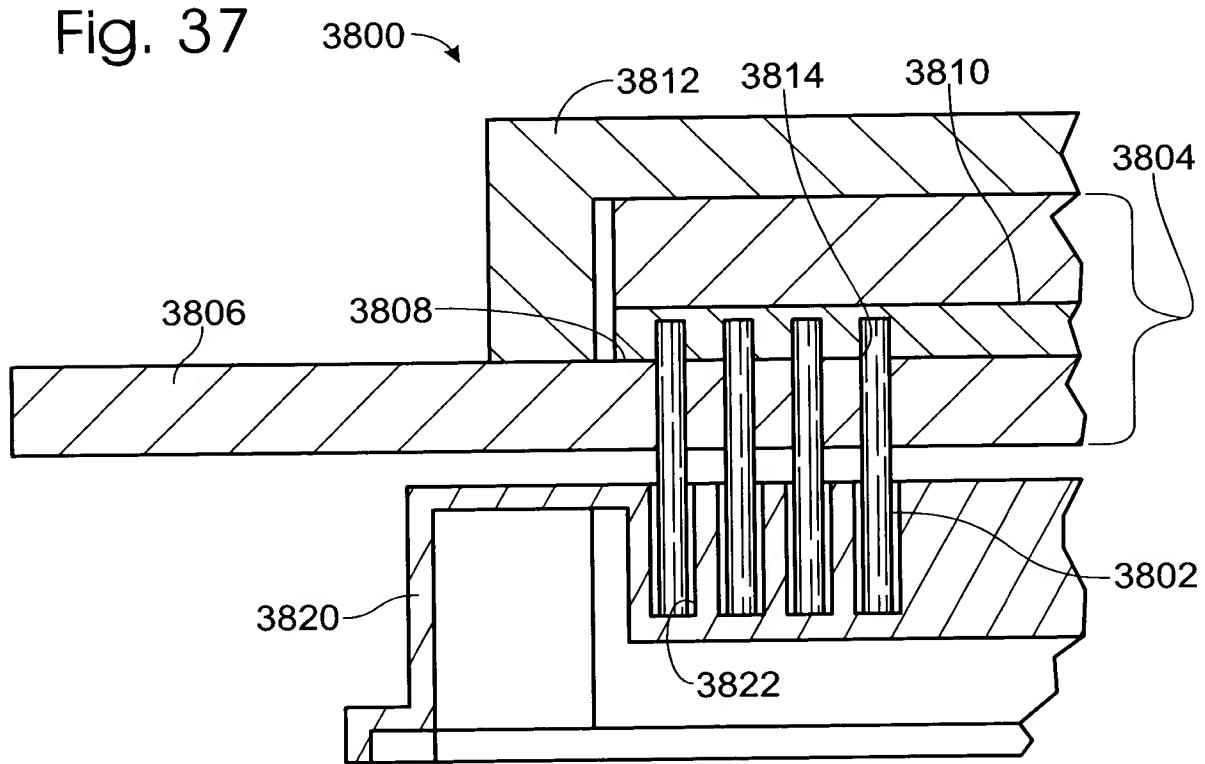


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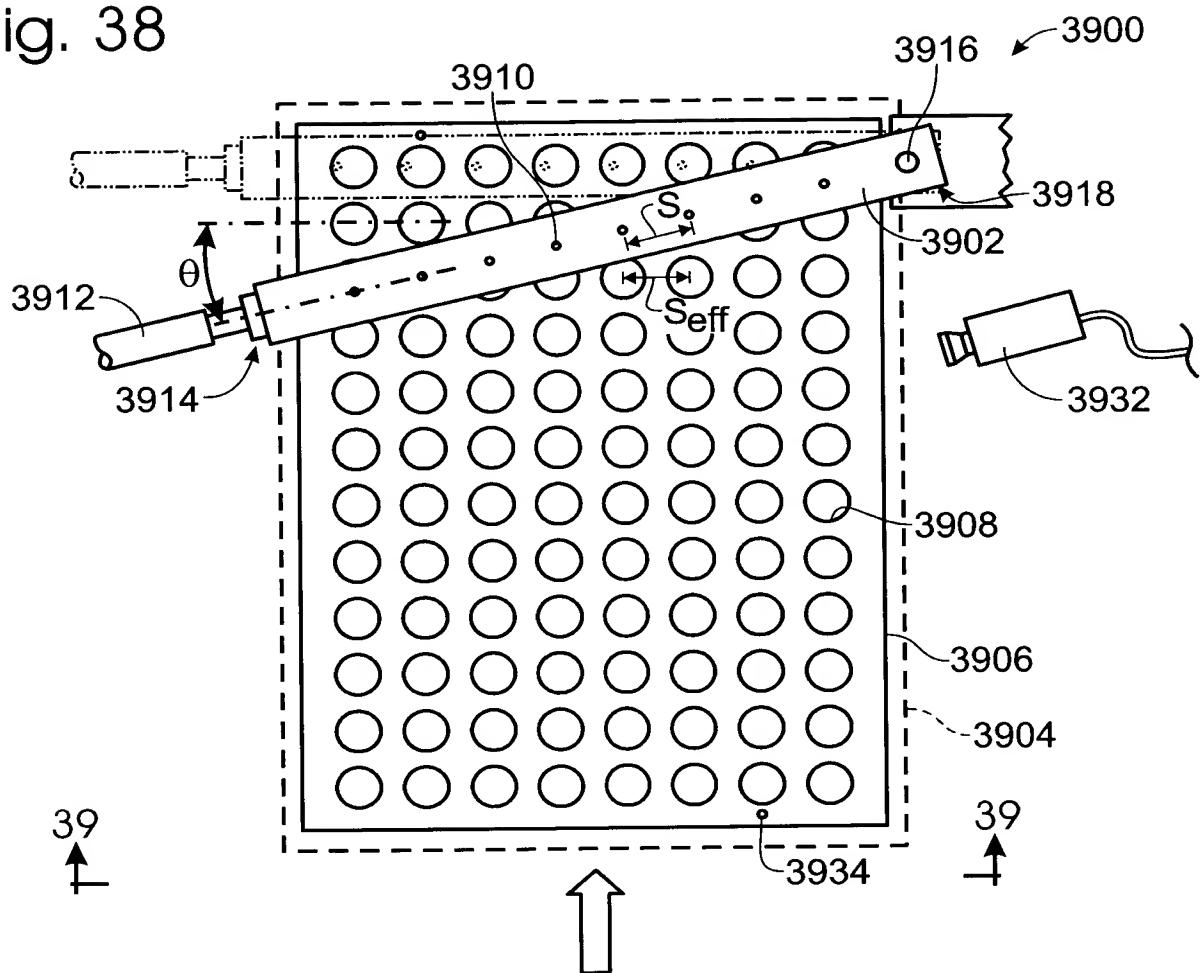


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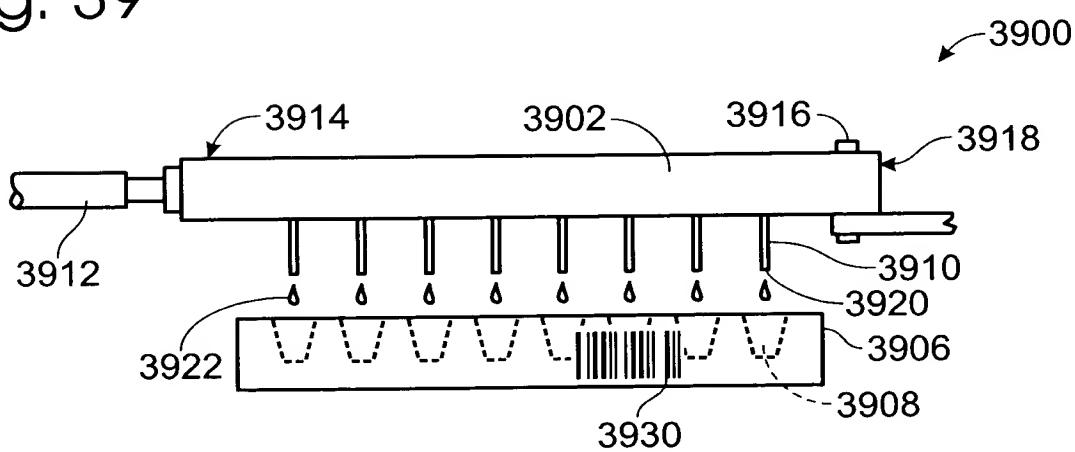


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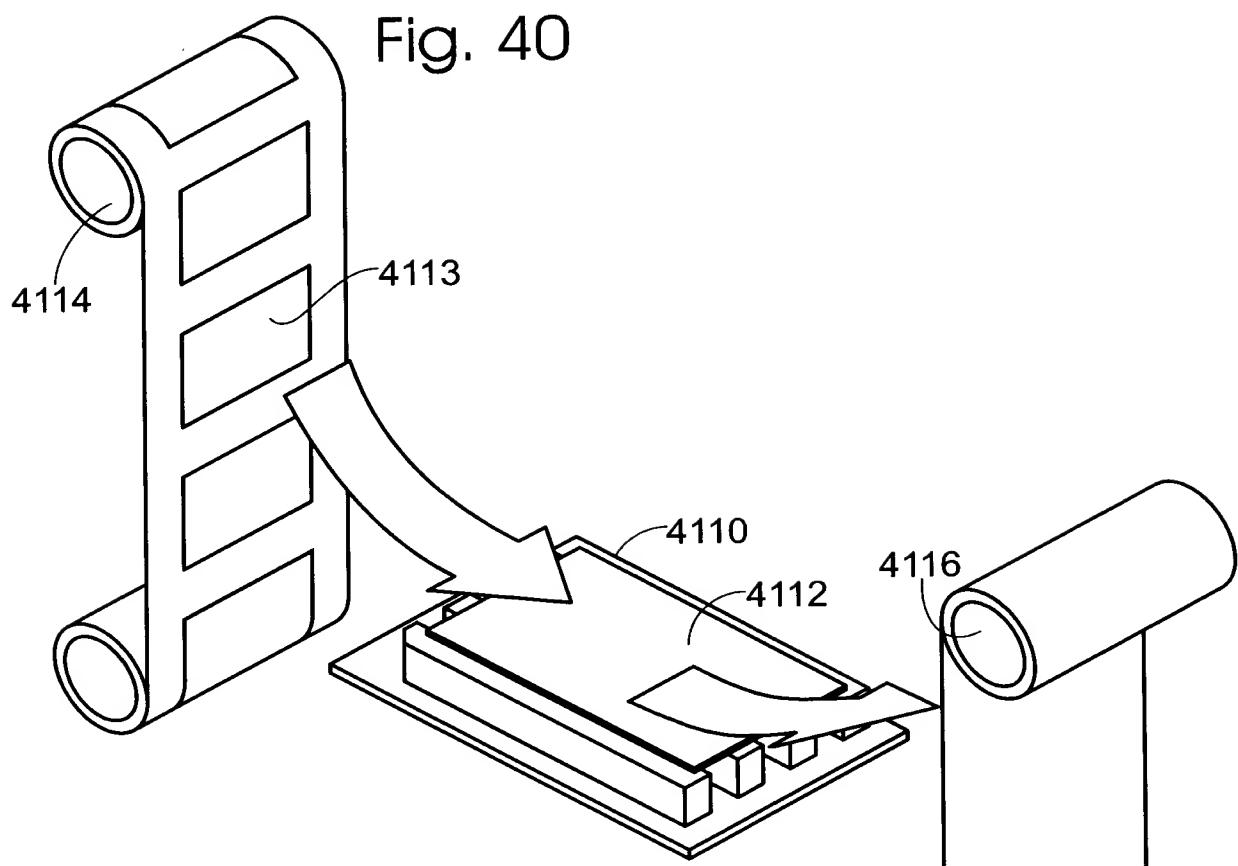


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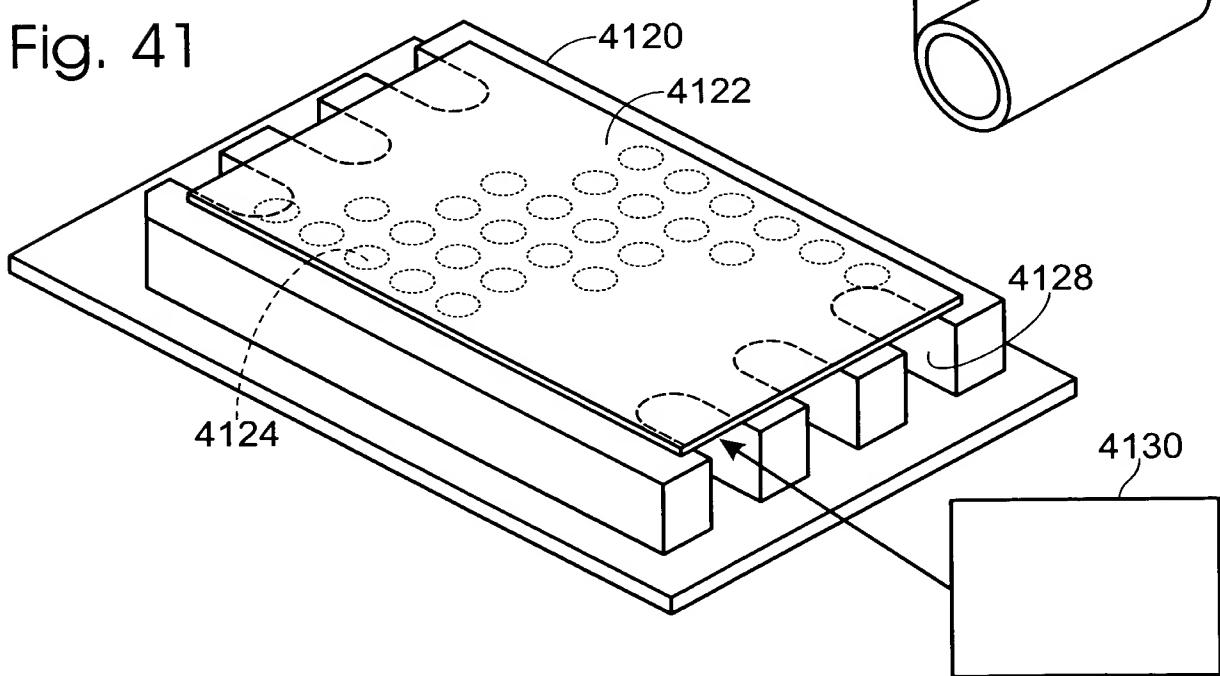


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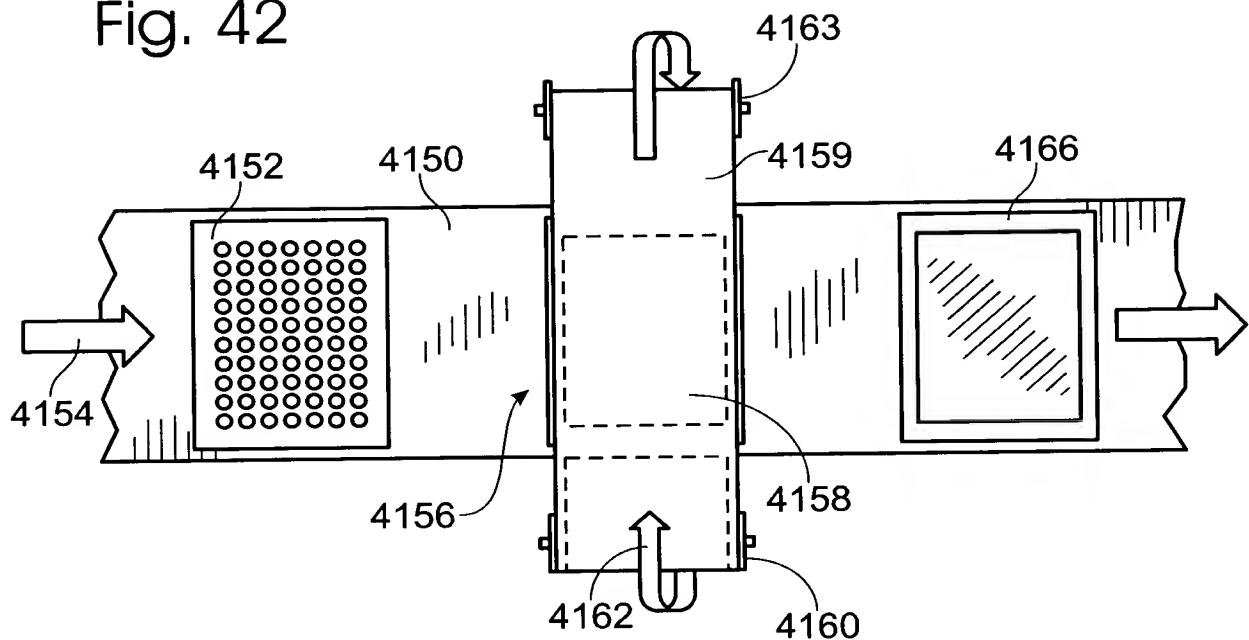


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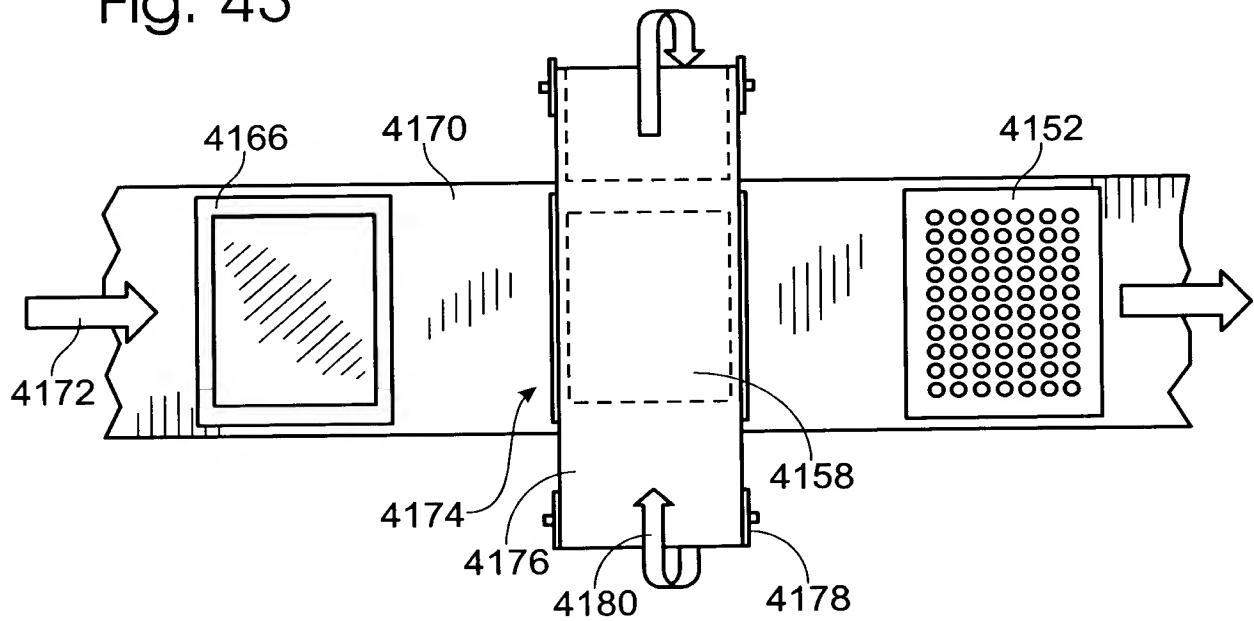


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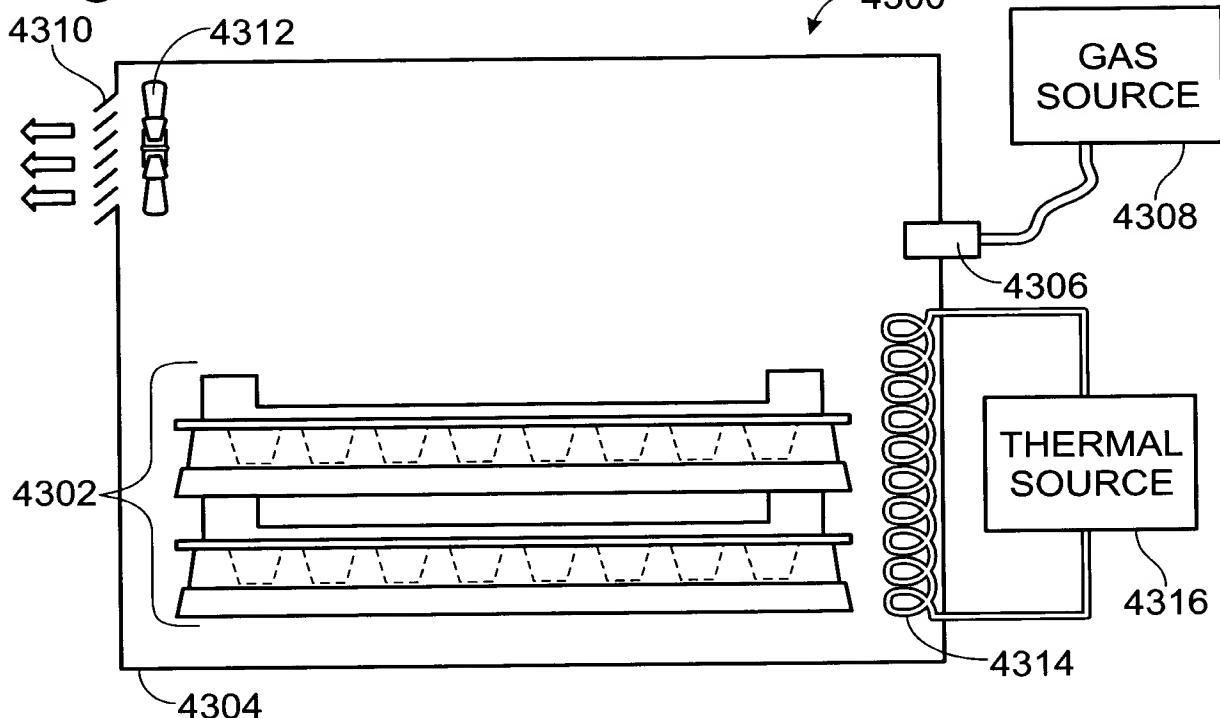


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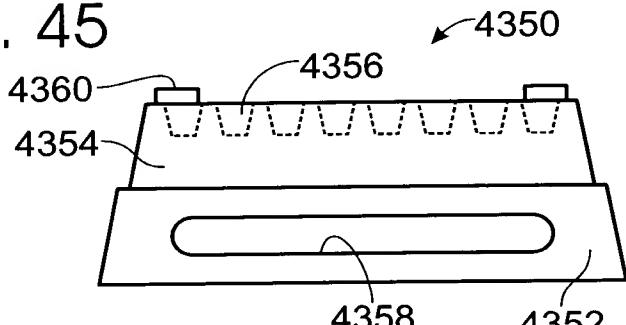


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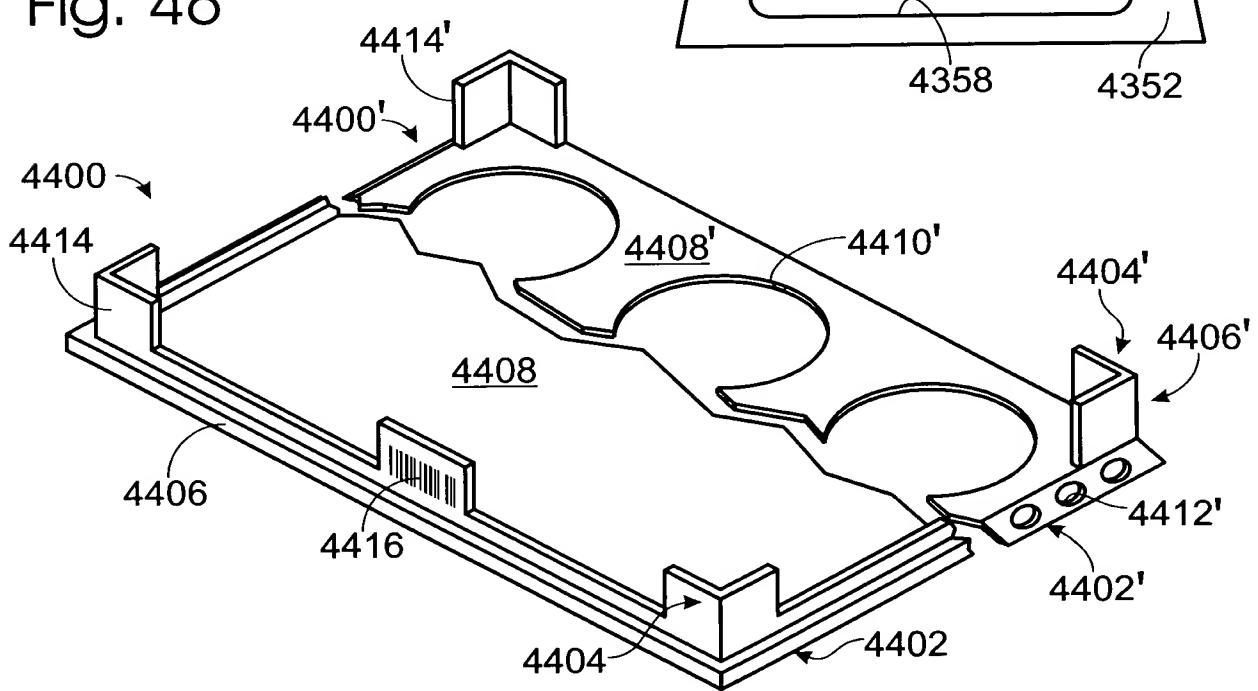


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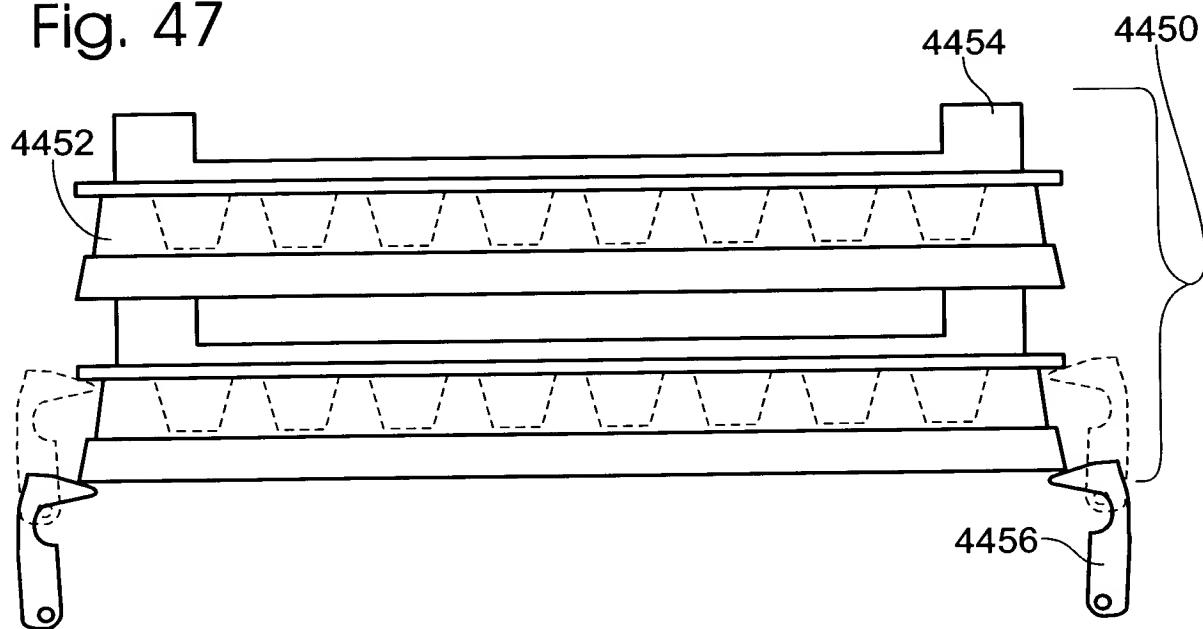


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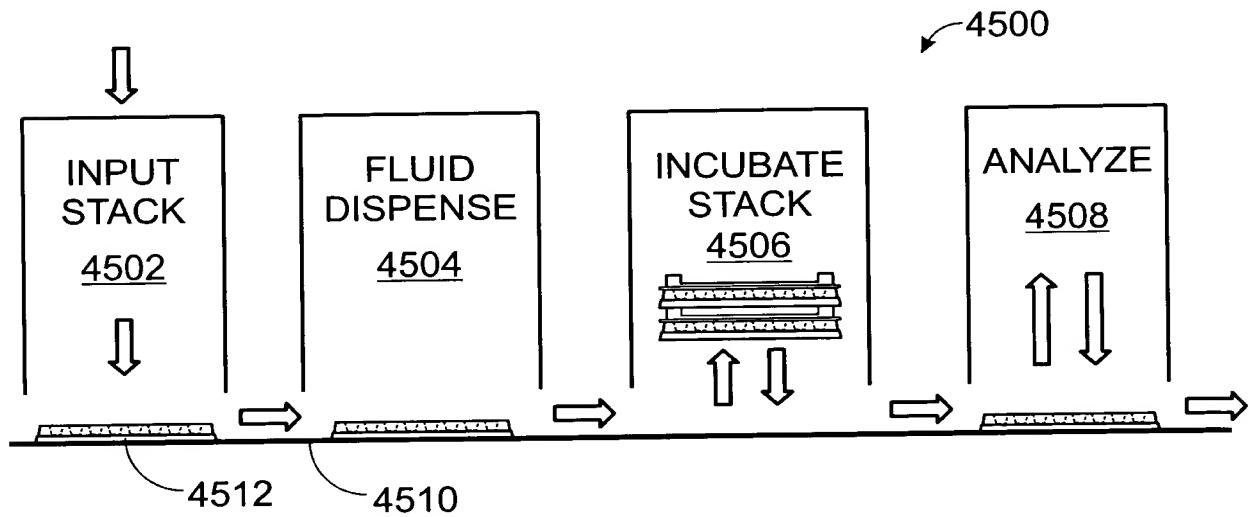
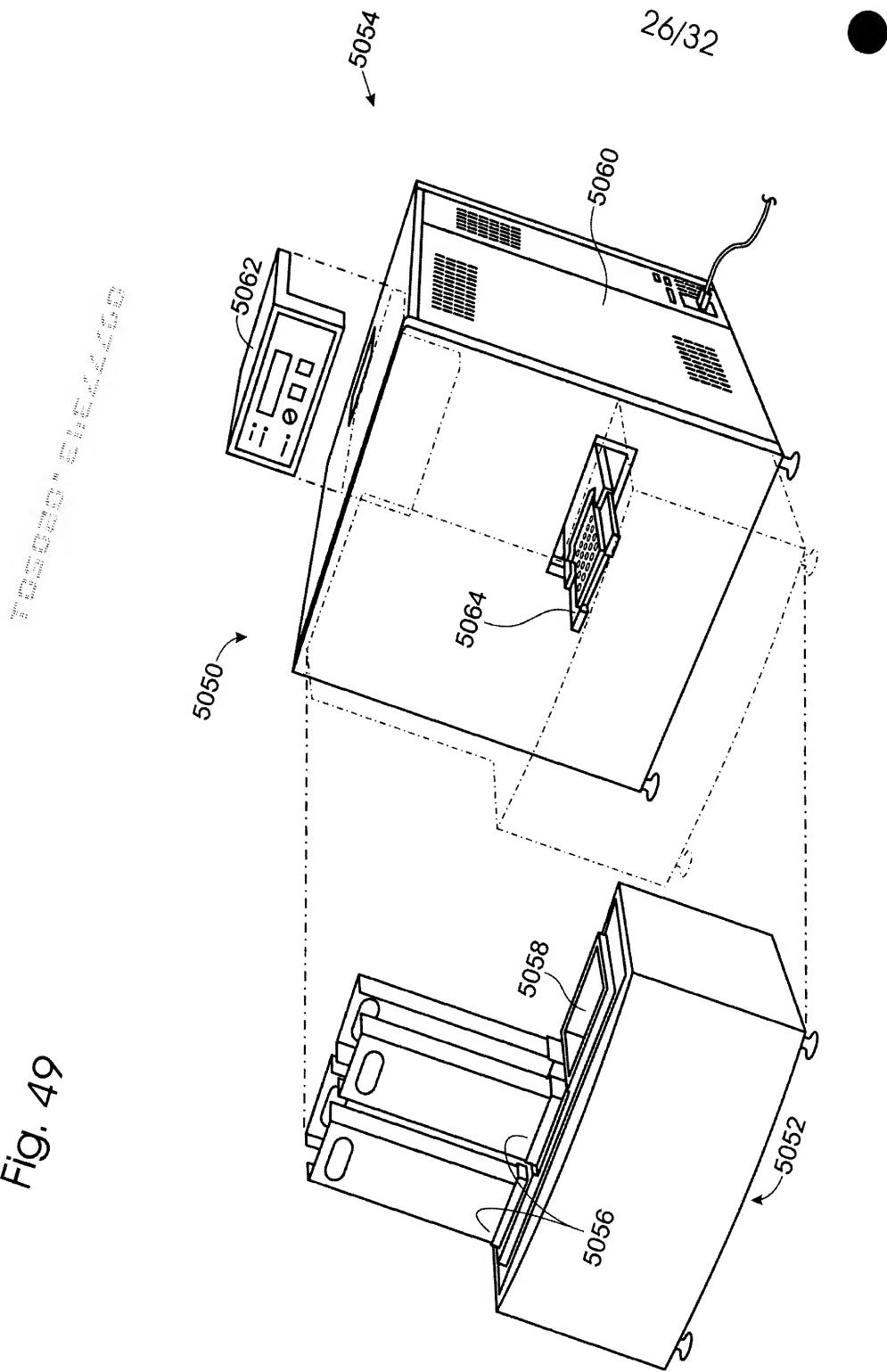


Fig. 49



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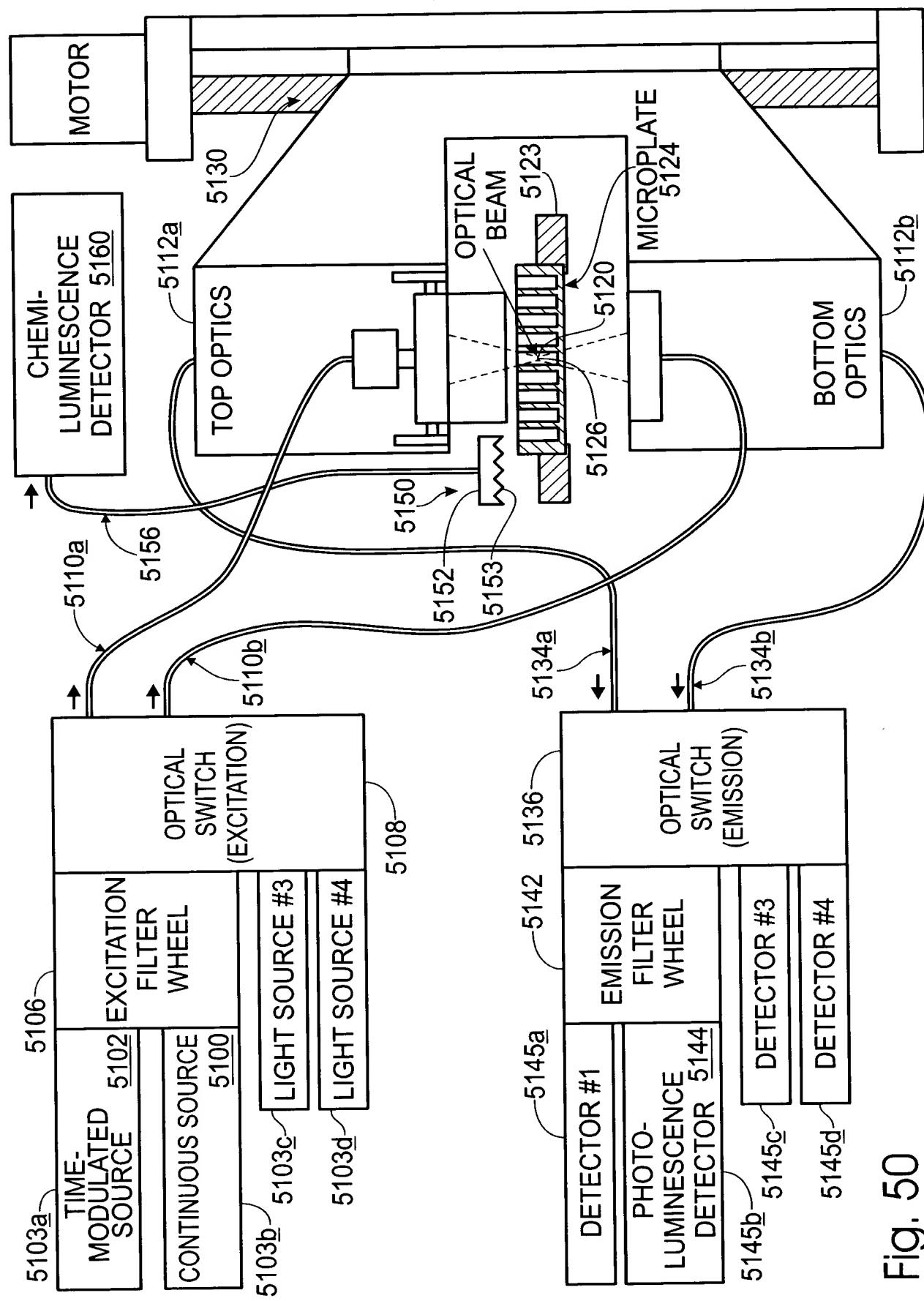


Fig. 50

Fig. 51

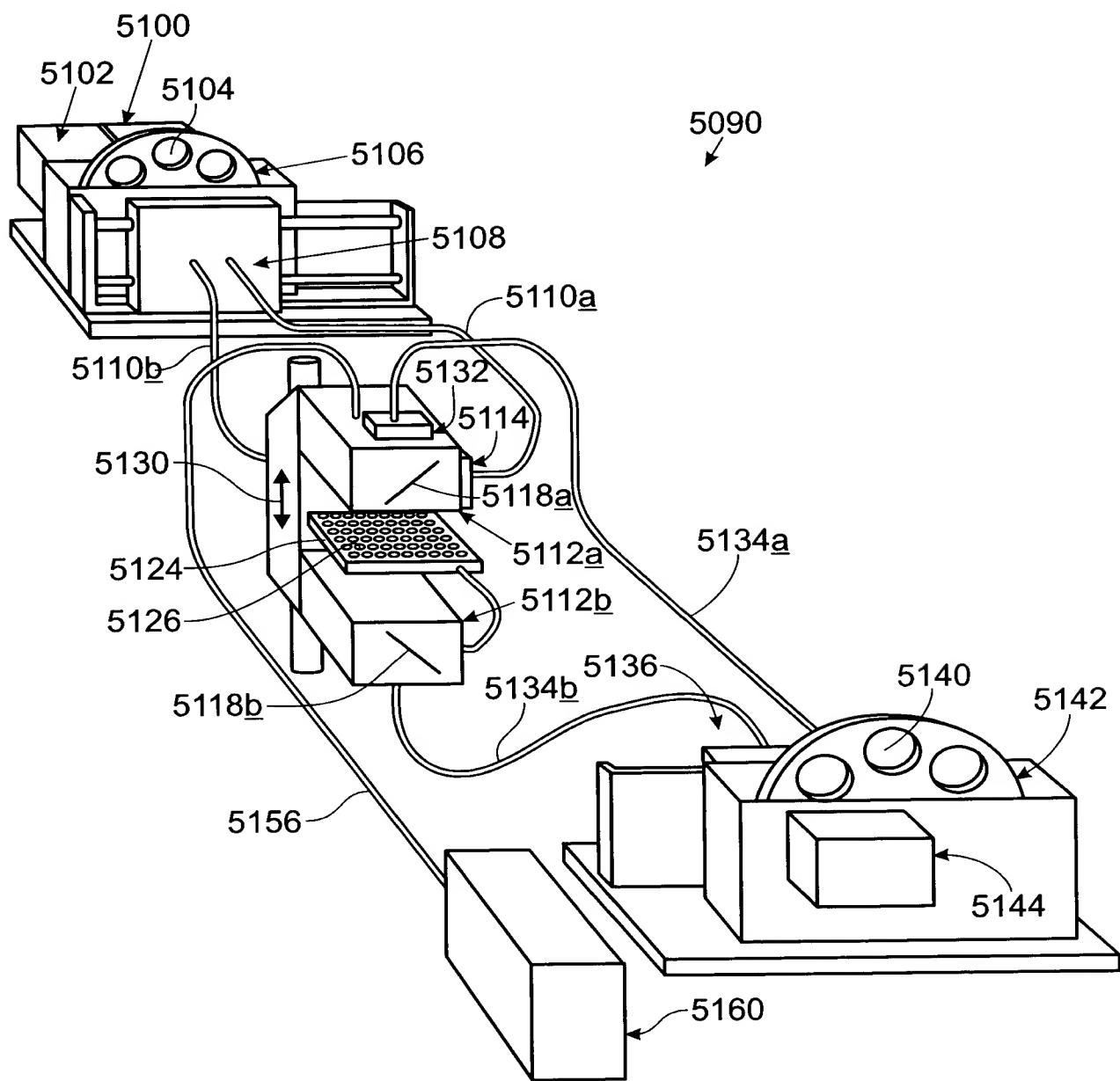


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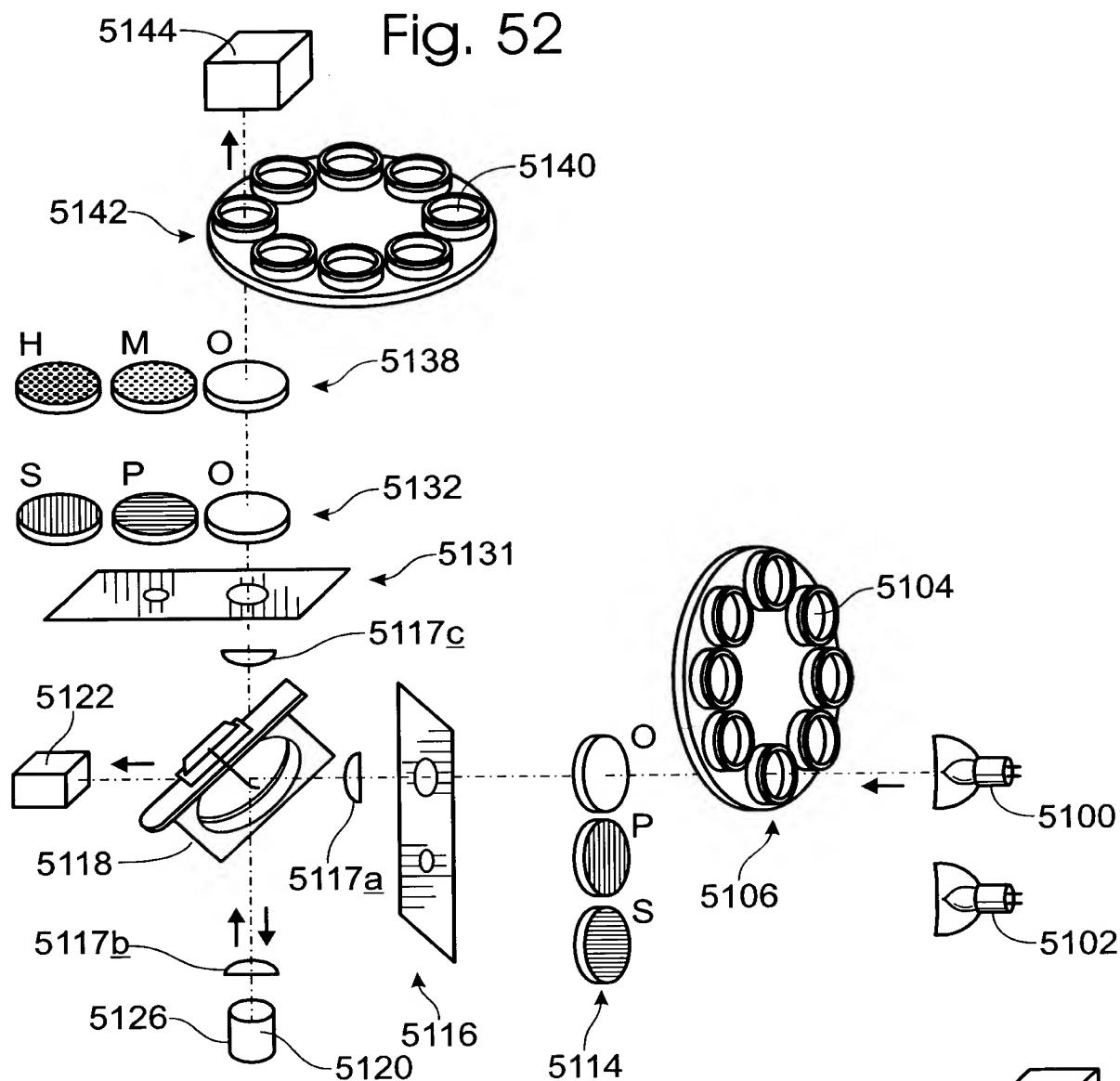


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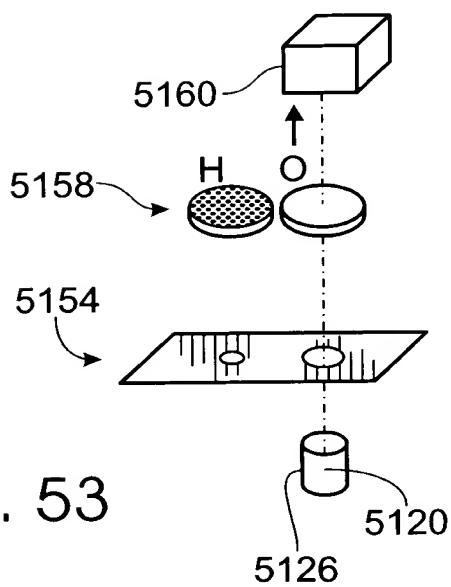


Fig. 54

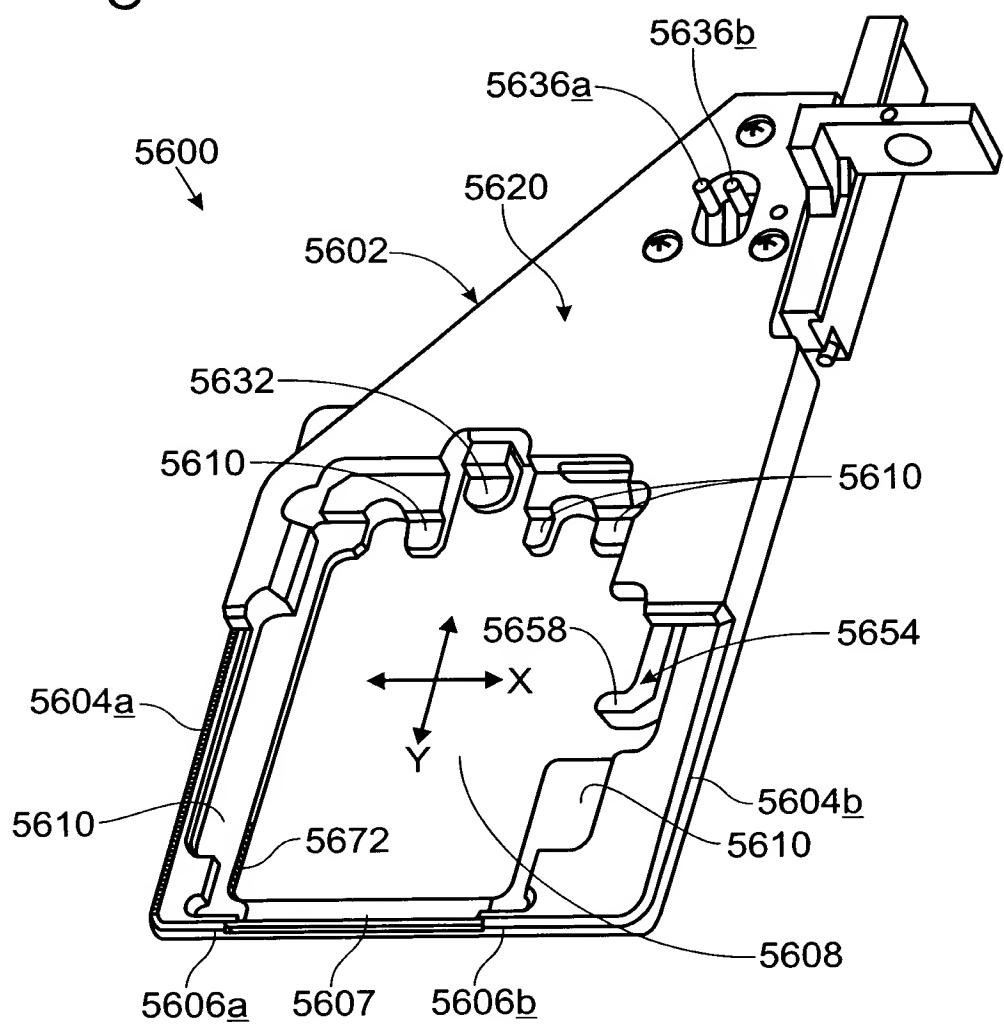


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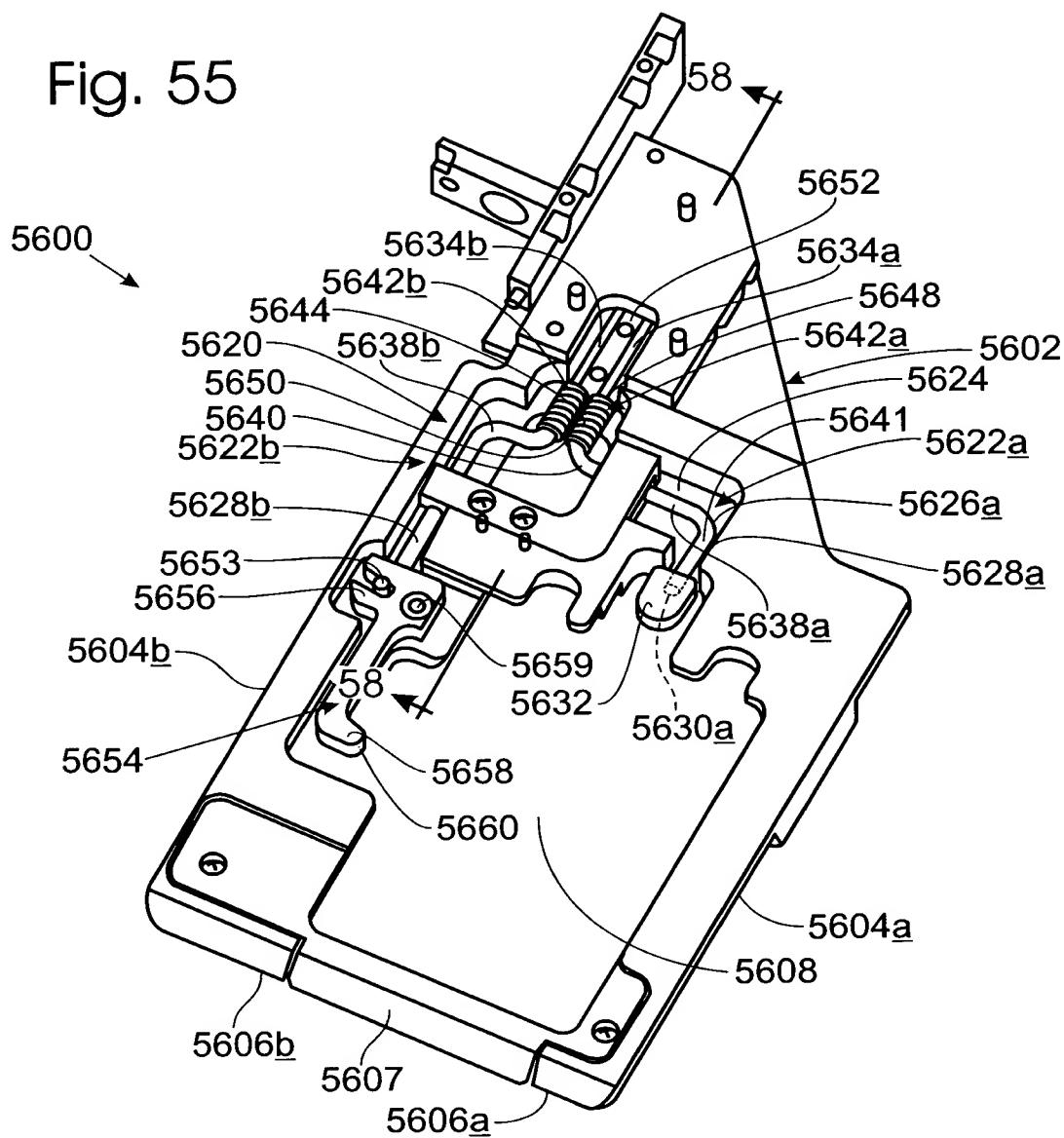
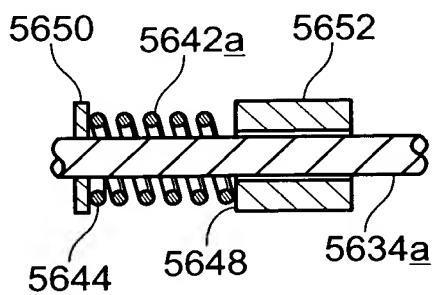


Fig. 56



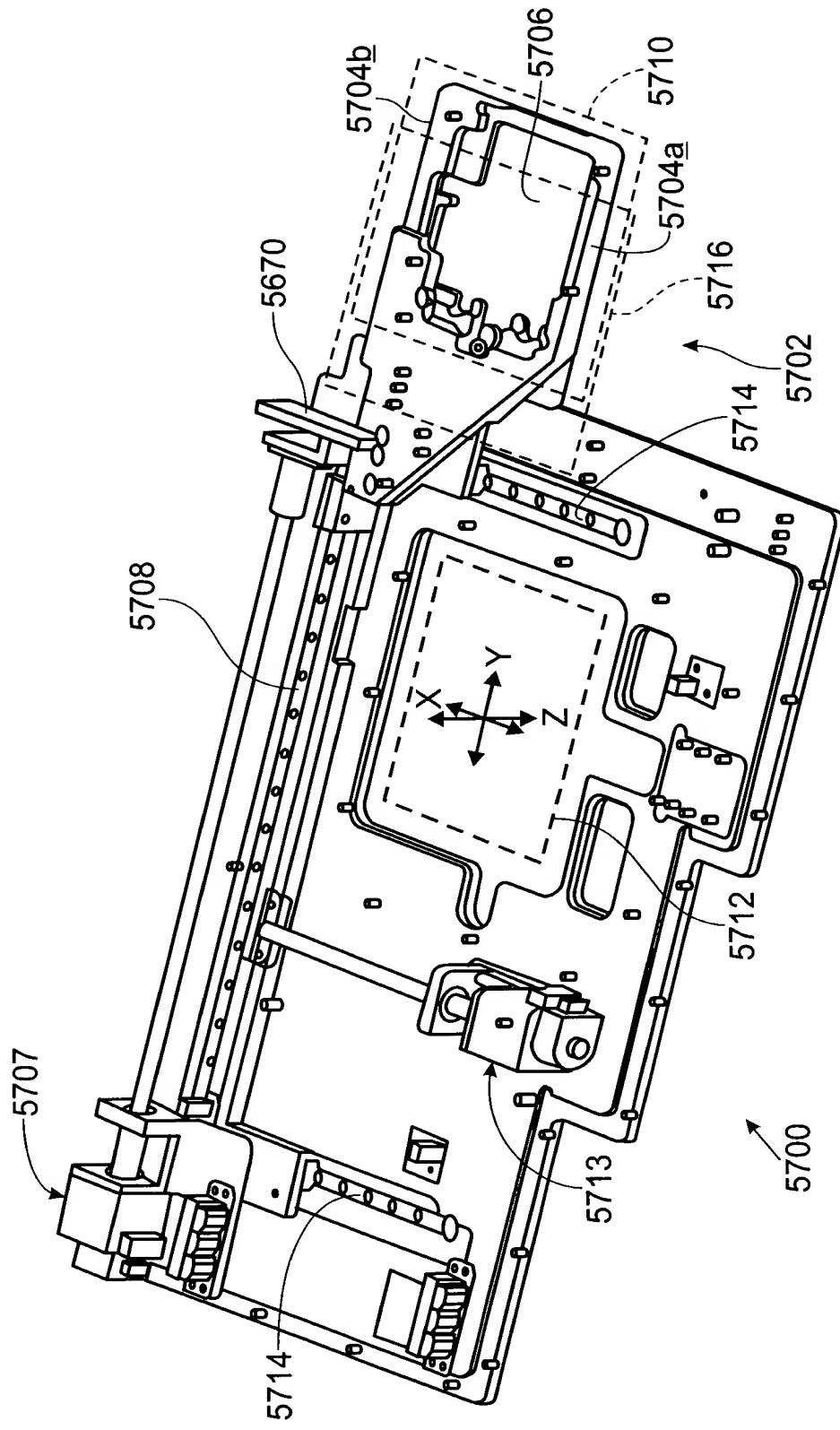


Fig. 57